

(19)



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European Patent Office
Office européen des brevets



(11)

EP 1 193 866 A1

(12)

EUROPEAN PATENT APPLICATION

published in accordance with Art. 158(3) EPC

(43) Date of publication:

03.04.2002 Bulletin 2002/14

(51) Int Cl.⁷: **H03F 1/32**(21) Application number: **99922541.0**

(86) International application number:

PCT/JP99/02824(22) Date of filing: **28.05.1999**

(87) International publication number:

WO 00/74232 (07.12.2000 Gazette 2000/49)(84) Designated Contracting States:
DE FR GB(71) Applicant: **FUJITSU LIMITED**
Kawasaki-shi, Kanagawa 211-8588 (JP)

(72) Inventors:

- **MANIWA, Toru Fujitsu Limited**
Kawasaki-shi, Kanagawa 211-8588 (JP)

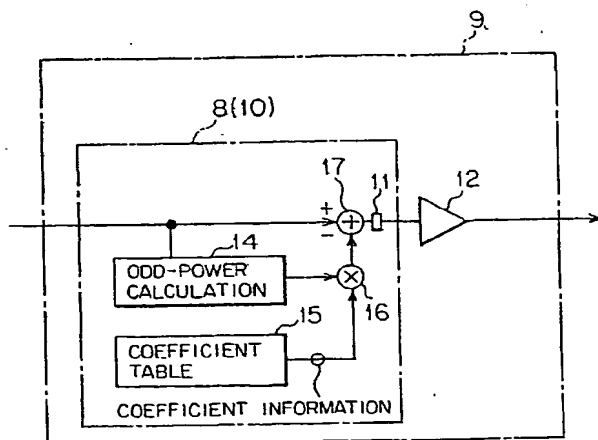
- **HASEGAWA, Tsuyoshi Fujitsu Limited**
Kawasaki-shi, Kanagawa 211-8588 (JP)
- **KAWASAKI, Yoshihiro Fujitsu Limited**
Kawasaki-shi, Kanagawa 211-8588 (JP)

(74) Representative: **HOFFMANN - EITLE**
Patent- und Rechtsanwälte
Arabellastrasse 4
81925 München (DE)

(54) **PREDISTORTION TYPE DISTORTION COMPENSATION AMPLIFIER**

(57) In the mobile communication, a distortion compensation amplifying apparatus is provided. In a predistortion-type distortion compensation amplifying apparatus (9), a signal amplitude change processing unit (10) comprises an odd-power component calculating unit (14) extracting a power component of odd order of an amplitude quantity contained in a signal to be transmitted and outputting it, a coefficient information outputting unit (15) being operable to select one of plural pieces of coefficient information based on a signal from the outside and output it, a multiplying unit (16) multiplying a

signal outputted from the odd-power component calculating unit (14) by coefficient information from the coefficient information outputting unit (15) and outputting an attenuation signal, and an adding unit (17) subtracting the attenuation signal from the signal to be transmitted and outputting a result, thereby making an amplifier operate within a range not exceeding a saturation region thereof, decreasing a spurious signal, decreasing an adjacent channel power, and enabling distortion compensating operation even if a frequency of reference to the memory is decreased.

FIG. 2

Description

Technical Field

5 [0001] The present invention relates to a predistortion-type distortion compensation amplifying apparatus suitable for use in a high-frequency circuit for mobile communication.

Background Art

10 [0002] In mobile communication, a digital modulation system is used to increase the efficiency of frequency utilization, etc. In such case, distortion caused by non-linearity of characteristics of a power amplifier disturbs the adjacent channel, which is a problem. In order to prevent disturbance to the adjacent channel, a power amplifier having a low adjacent channel power (ACP: Adjacent Channel Power) is required. However, a use of the power amplifier within a linear operation region is not always advisable in view of the circuit scale and cost. Instead, a predistortion (hereinafter also referred as distortion compensation) is used in many cases.

15 [0003] Predistortion is a method of beforehand distorting an input signal to be amplified using a function representing a reverse characteristic of an input-output characteristic of the amplifier (power amplifier: hereinafter, the power amplifier being simply referred as an amplifier occasionally) when the signal is inputted to the amplifier. Namely, predistortion is a technique of beforehand distorting an input signal to be amplified and amplifying it, whereby a signal which has been linearized appears at the output of the amplifier.

20 [0004] FIG. 18 is a diagram showing an example of the radio transceiver using predistortion. In a radio transceiver 50 shown in FIG. 18, a baseband signal to be transmitted is modulated and predistorted in a digital signal processor (DSP: Digital Signal Processor) 50a, where a distortion compensation coefficient operating process is performed to estimate non-linear distortion of a power amplifier 50c. In a quadrature modulating-demodulating unit 50b, the processed baseband signal is up-converted into the RF (Radio Frequency) band. In the power amplifier 50c, a predetermined power is applied to the signal, and the signal is fed to an antenna 50e via a synthesizer 50d and transmitted.

25 [0005] On the other hand, the modulated signal fed back from a part of the signal outputted from the power amplifier 50c is down-converted into a baseband signal having a distorted component in the quadrature modulating-demodulating unit 50b. The converted signal is inputted to the digital signal processor 50a, where a distortion compensation coefficient operating process is performed. Accordingly, an RF signal free of distortion is outputted from the antenna 50e by this loop process. In the structure shown in FIG. 18, predistortion is performed in the baseband. Signal processing in the baseband will be now described with reference to FIG. 19, corresponding to equations.

30 [0006] FIG. 19 is a diagram showing an example of a known predistortion circuit (also referred as a predistorter). A quadrature modulator 60 shown in FIG. 19 performs the distortion compensation coefficient operating process, which comprises memories 61 and 62, multipliers 63a, 63b, 63c and 64d, and adders 64a and 64b. Baseband signals I and Q are undergone the distortion compensation operating process by referring to the respective memories 61 and 62 (memory regions denoted by reference characters 61a, 61b, 61c and 61d, and memory regions denoted by reference characters 62a, 62b, 62c and 62d) in a software process or the like. The signals processed in these memories 61 and 62 are multiplied in the multipliers 63a-63d, added in the adders 64a and 64b, and I_{pd} and Q_{pd} are outputted.

35 [0007] When an output of an amplifier is $P_o(t)$, the output is expressed by a product of a function $f(t)$ of amplitude and a function $g(t)$ of phase as shown in Equation (1):

$$P_o(t) = f\{M_i(t)\} \cdot \exp[-j \cdot g\{M_i(t)\}] \cdot \exp(j\omega t) \quad (1)$$

45 where, $M_i(t)$ is magnitude of amplitude of a modulated wave, ω is a center frequency, t is a time, and j is an imaginary unit which represents $j^2 = -1$.

[0008] When input signals to the quadrature modulator 60 are $I(t)$ and $Q(t)$, magnitude $x(t)$ of amplitude of a modulated wave inputted to the amplifier are expressed by Equation (2) (operation in the memory regions 61a and 62a shown in FIG. 19):

$$X(t) = \sqrt{I(t)^2 + Q(t)^2} \quad (2)$$

55 [0009] Magnitude $y(t)$ of amplitude of a modulated wave component outputted from the amplifier is expressed, with G being a gain, by Equation (3):

$$y(t)=G \cdot x(t) \quad (3)$$

[0010] FIG. 12 is a diagram showing an example of the input-output characteristic of the amplifier. A part denoted by B1 in FIG. 12 is a non-saturation region, whereas a part denoted by B2 is a saturation region. The input-output characteristic changes its characteristic at a point denoted by A, which is a function having an upper limit because of the saturation characteristic of the amplifier. In order to compensate the distortion of an output signal amplified as expressed by Equation (3), distortion compensation using an inverse function $f^{-1}(t)$ of the function $f(t)$ of amplitude within the quadrature modulator 60 is performed. An output $P_{pd}(t)$ of the amplifier undergone the distortion compensation is expressed by Equation (4):

$$P_{pd}(t)=f^{-1}(y) \cdot \exp[j \cdot g\{f^{-1}(y)\}] \cdot \exp(\omega t) \quad (4)$$

[0011] Namely, $I_{pd}(t)$ and $Q_{pd}(t)$ which are deformed $I(t)$ and $Q(t)$ (hereinafter abbreviated as I_{pd} and Q_{pd} , respectively) are expressed by Equations (5) and (6), respectively:

$$I_{pd}=\{f^{-1}(y)/x\} \cdot [I \cos\{g\{f^{-1}(y)\}\} - Q \sin\{g\{f^{-1}(y)\}\}] \quad (5)$$

$$Q_{pd}=\{f^{-1}(y)/x\} \cdot [Q \cos\{g\{f^{-1}(y)\}\} - I \sin\{g\{f^{-1}(y)\}\}] \quad (6)$$

where $x(t)$ and $y(t)$ are abbreviated as x and y . Generally, input-output relationships of the Equations (5) and (6) are stored in the memories 61 and 62 as a reverse characteristic of the characteristic as shown in FIG. 12. Values of $I(t)$ and $Q(t)$ are, for example, very frequently referred at sampling time intervals for digital signals, and the outputs I_{pd} and Q_{pd} are obtained.

[0012] Namely, y is determined from data in the memory region 61b shown in FIG. 19, then $f^{-1}(y)/x$ in the Equations (5) and (6) is calculated from y and data in the memory region 61c. Further, $\{f^{-1}(y)/x\} \cdot I$ in the Equation (5) and $\{f^{-1}(y)/x\} \cdot Q$ in the Equation (6) are calculated from data in the memory region 62b, and outputted. Similarly, y is determined from data in the memory region 62b, then $g\{f^{-1}(y)\}$ in the Equations (5) and (6) is calculated from y and data in the memory region 62c. Further, $\cos\{g\{f^{-1}(y)\}\}$ and $\sin\{g\{f^{-1}(y)\}\}$ in the Equations (5) and (6) are calculated from data in the memory region 62d, and outputted. These outputs are added in the multipliers 63a-63d, after which, added in the adders 64a and 64b, and deformed I_{pd} and Q_{pd} are outputted. As this, distortion compensation is performed using a predistortion circuit as the quadrature modulator 60 to make the amplification characteristic of the amplifier linear, in general.

[0013] Incidentally, as a method for compensating a frequency characteristic in the vicinity of higher harmonic in bias circuits of the amplifier and the power circuit (not shown), and a matching circuit, there has been proposed a method of determining a coefficient from a differential value or an integral value of an amplitude quantity of an input signal, and multiplying the original signal by the coefficient to obtain a predistortion signal.

[0014] FIG. 20 is a diagram showing an example of a predistortion circuit using a differential value or an integral value of an input signal. In a predistortion circuit 70 shown in FIG. 20, magnitude of amplitude of a demodulated wave with respect to input signals I and Q is calculated in an amplitude calculating unit 70a, $f^{-1}(y)$ is calculated in an inverse function calculating unit 70b, a differential value or an integral value of an amplitude quantity of the input signals is calculated in a differential/integral coefficient information adding unit 70c, an appropriate coefficient is obtained in a coefficient table 70d, the coefficient is multiplied in each of multipliers 70e and 70f, then I_{pd} and Q_{pd} are outputted.

[0015] However, when linearization by Equation (3) is performed in the circuits shown in FIGS. 19 and 20, distortion increases. FIG. 23 is a diagram showing an input-output characteristic of a linearized amplifier. Since the input-output characteristic becomes discontinuous at a point (refer to a point A) where the output changes to the saturation region as shown in FIG. 23, distortion increases when the input signal steps into this region, which is a problem. This distortion will be now described with reference to FIGS. 21(a), 21(b), 22(a) and 22(b).

[0016] FIG. 21(a) is a diagram showing an example of a signal wave in the case where two waves having an equal amplitude are inputted. FIG. 21(b) is a diagram showing a spectrum of the signal shown in FIG. 21(a). As shown in FIG. 21(b), this signal has components of only frequencies f_1 and f_2 .

[0017] FIG. 22(a) is a diagram showing an example of a signal waveform in the case where the signal shown in FIG. 21(a) is amplified by an amplifier having the input-output characteristic shown in FIG. 23. FIG. 22(b) is a diagram showing a spectrum of the signal shown in FIG. 22(a). As shown in FIG. 22(b), there is a problem which a spurious

signal generates over a wide range of the frequency.

[0018] Moreover, since the frequency of reference to the memory is equal to a sampling time for the signal, the memory reference cannot catch up with in the case of a signal changing at a high speed, which leads to a difficulty in applying it to a high-speed signal. There is another problem which a predistortion circuit as shown in FIG. 19 generates an error in a coefficient to be referred when the characteristic varies due to a change in temperature or a change with time of the amplifier.

[0019] In the light of the above problems, the present invention provides a predistortion-type distortion compensation amplifying apparatus not requiring an external RF circuit having a linear amplification characteristics such as a feed-forward-type amplifier, which can decrease a spurious signal by adding an odd-power component to an input signal to make the amplifier operate within a range not exceeding the saturation region of the amplifier, decrease the adjacent channel power by multiplying the odd-power component of the input signal by a coefficient (coefficient information) and adding the result to the input signal, and perform a distortion compensating operation even if the frequency of reference to the memory is decreased.

Disclosure of Invention

[0020] A predistortion-type distortion compensation amplifying apparatus according to this invention comprises an amplifier for amplifying a signal to be transmitted, and a signal processing unit provided on the front side of the amplifier to perform a coefficient changing process to change coefficient information of a power component of an amplitude quantity contained in the signal to be transmitted, and being operable to output a processed signal.

[0021] Accordingly, it is possible to decrease spurious signals generating in a wide range of a frequency, and decrease the adjacent channel power. Additionally, even if characteristics of the amplifier vary due to a change in temperature or a change with time, it is possible to cope with it by comparing a part of an output signal and a part of an input signal, calculating an error therebetween, and rewriting a coefficient table when the error is large.

[0022] The signal processing unit performs a process with a function representing a reverse characteristic of an input-output characteristic of the amplifier on the signal to be transmitted, performs a coefficient changing process, and makes an odd-order component appear in an output signal of the amplifier, and the amplifier operates in a non-saturation region.

[0023] Accordingly, it is possible to prevent spurious from spreading over a wideband, and perform distortion compensation without an external RF circuit having a linear amplification characteristic. It is also possible to contribute to a reduction of the power consumption and allow compactness of the circuit scale. Further, a frequency characteristic in the vicinity of higher harmonic in a bias circuit of the amplifier, a bias circuit of the power circuit or a matching circuit, which leads to improvement of the accuracy.

[0024] The signal processing can decrease contribution of a power component of odd order of an amplitude quantity contained in the signal to be transmitted when performing a coefficient changing process.

[0025] Accordingly, the adjacent channel power which is a problem in a spurious signal is decreased, an external RF circuit having a linear amplification characteristic becomes unnecessary, and the circuit scale can be reduced, which leads to contribution to a reduction of the power consumption and allows compactness of a portable telephone or the like. Frequency of reference to the coefficient table becomes equal to a time for switching an average output power, it is thus possible to form the circuit with a memory having a relatively slow access speed.

Brief Description of Drawings

[0026]

FIG. 1 is a block diagram of a predistortion-type distortion compensation amplifying apparatus in a transmitting unit of a radio transmitter to which the present invention is applied;

FIG. 2 is a block diagram of a predistortion-type distortion compensation amplifying apparatus according a first embodiment of this invention;

FIG. 3 is a block diagram showing in more detail the predistortion-type distortion compensation amplifying apparatus according to the first embodiment of this invention;

FIG. 4 is a block diagram of a predistortion-type distortion compensation amplifying apparatus according to a first modification of the first embodiment of this invention;

FIG. 5 is a block diagram showing in more detail the predistortion-type distortion compensation amplifying apparatus according to the first modification of the first embodiment of this invention;

FIG. 6 is a block diagram of a predistortion-type distortion compensation amplifying apparatus according to a second modification of the first embodiment of this invention;

FIG. 7 is a block diagram showing in more detail the predistortion-type distortion compensation amplifying apparatus

ratus according to the second modification of the first embodiment of this invention;

FIG. 8 is a block diagram of a predistortion-type distortion compensation amplifying apparatus according to a third modification of the first embodiment of this invention;

FIG. 9 is a block diagram showing in more detail the predistortion-type distortion compensation amplifying apparatus according to the third modification of the first embodiment of this invention;

FIG. 10 is a block diagram showing a transmitting unit of a radio transmitter to which a predistortion-type distortion compensation amplifying apparatus according to a second embodiment of this invention is applied;

FIG. 11 is a block diagram of the radio transmitter according to the second embodiment of this invention;

FIG. 12 is a diagram showing an example of an input-output characteristic of an amplifier;

FIG. 13(a) is a diagram showing a signal waveform having an envelope composed of a fundamental wave and a third-order component;

FIG. 13(b) is a diagram showing a signal spectrum of an output waveform in the case where the signal waveform is amplified by an amplifier;

FIG. 14(a) is a diagram showing a signal waveform having an envelope composed of a fundamental wave and third-order and fifth-order components;

FIG. 14(b) is a diagram showing a signal spectrum of an output waveform in the case where the signal waveform is amplified by an amplifier;

FIG. 15 is a diagram showing a result of comparison between normal linearization and linearization according to this embodiment in PDC;

FIG. 16 is a diagram showing a result of comparison between normal linearization and linearization according to this embodiment in CDMA;

FIG. 17 is a block diagram showing a transmitting unit of a radio transmitter to which a predistortion-type distortion compensation amplifying apparatus according to a first modification of the second embodiment of this invention is applied;

FIG. 18 is a diagram showing an example of a radio transceiver using predistortion;

FIG. 19 is a diagram showing an example of a known predistortion circuit;

FIG. 20 is a diagram showing an example of a predistortion circuit using a differential value or an integral value of an input signal;

FIG. 21(a) is a diagram showing an example of a signal waveform in the case where two waves having an equal amplitude are inputted;

FIG. 21(b) is a diagram showing a spectrum of the signal;

FIG. 22(a) is a diagram showing an example of a signal waveform in the case where a signal waveform is amplified by an amplifier;

FIG. 22(b) is a diagram showing a spectrum of a signal; and

FIG. 23 is a diagram showing an input-output characteristic of a linearized amplifier.

Best Mode for Carrying Out the Invention

(A) Description of First Embodiment of the Invention

[0027] FIG. 1 is a block diagram of a predistortion-type distortion compensation amplifying apparatus in a transmitting unit of a radio transmitter, to which the present invention is applied. A radio transmitter 7 shown in FIG. 1 comprises a predistortion-type distortion compensation amplifying apparatus 9 and an antenna 13. The predistortion-type distortion compensation amplifying apparatus 9 beforehand distorts a digital baseband signal (signal to be transmitted) inputted from an external circuit with a function representing a reverse characteristic of an input-output characteristic of an amplifier 12, then amplifies it. The predistortion-type distortion compensation amplifying apparatus 9 comprises a signal processing unit 8 and the amplifier 12. The antenna 13 transmits a radio signal outputted from the predistortion-type distortion compensation amplifying apparatus 9. A signal inputted from the left side in FIG. 1 is not directly amplified by the amplifier 12 and transmitted, but is added distortion thereto in the signal processing unit 8 and transmitted. The antenna 13 also has, for example, a receiving function, detailed description of which is omitted. Similarly, with respect to parts which will be described later in each of embodiments and modifications and have also functions of the receiving system, detailed descriptions of receiving functions of which will be omitted, only functions of the transmitting system will be described.

[0028] The amplifier 12 amplifies a signal to be transmitted. The signal processing unit 8 is disposed in the front stage of the amplifier 12 to perform a coefficient changing process to change coefficient information on a power component of an amplitude quantity contained in the signal to be transmitted, and can output a processed signal (predistortion signal). Here, the amplifier 12 signifies a power amplifier. In the following description, "amplification" will signify "power amplification".

[0029] "Amplitude quantity" concretely represents magnitude $x(t)$ (hereinafter abbreviated as x) of amplitude of a signal. "Power component" of the amplitude quantity represents a term of the third power $A_2 \cdot x^3$ or a term of the fifth power $A_4 \cdot x^5$ of the amplitude quantity x . The coefficient information is a coefficient A_2 or A_4 of the power component, which is a complex number including a phase component. These terms will be used in the same meanings in the following embodiments and modifications. In the following description, power will be referred as the third order or the third power, signifying the same contents.

[0030] FIG. 2 is a block diagram of a predistortion-type distortion compensation amplifying apparatus 9 according to a first embodiment of this invention. The predistortion-type distortion compensation amplifying apparatus 9 shown in FIG. 2 comprises a signal processing unit 8 and an amplifier 12. The signal processing unit 8 is configured as a signal amplitude change processing unit 10 which can output a processed signal so as to decrease contribution of power components of odd orders of an amplitude quantity contained in a signal to be transmitted. The signal amplitude change processing unit 10 comprises an odd-power (odd-order) component calculating unit 14, a coefficient table (coefficient information outputting unit) 15, a multiplying unit 16, an adding unit 17, and a D/A converting unit 11.

[0031] The odd-order component calculating unit 14 extracts power components of odd orders of an amplitude quantity contained in a signal to be transmitted, and outputs them. The odd-order component calculating unit 14 extracts the components from a signal to be transmitted in software processing. Incidentally, the odd-order component calculating unit 14 comprises third-power component calculating units 14a and 14b, as will be described later.

[0032] The coefficient table (coefficient information outputting unit) 15 can select one of plural pieces of coefficient information according to a signal from the outside, and output it. For the coefficient table 15, a rewritable memory such as a RAM (Random Access Memory), a flash ROM (Flash Read Only Memory) or the like is used. As a value of the coefficient information, coefficient information according to an output power which is a target is stored. As will be described later, the coefficient table 15 comprises coefficient tables 15a and 15b.

[0033] When the output power is changed, coefficient information according to an output power which is a target is referred, and multiplied. Frequency of the reference is a unit of time for switching an average output power, which has no relation with a speed of a transmit signal. For this, it is possible to form the circuit with a memory having a relatively slow access speed. As a result, the coefficient table 15 is not required to operate at a high speed.

[0034] The multiplying unit 16 multiplies a signal outputted from the odd-order component calculating unit 14 by coefficient information from the coefficient table 15, and outputs an attenuation signal. As will be described later, the multiplying unit 16 comprises a plurality of portions for respective odd-order components. This function is accomplished by, for example, software.

[0035] The adding unit 17 subtracts the attenuation signal outputted from the multiplying unit 16 (a plurality of multiplying units as will be described later) from the above signal to be transmitted, and outputs a result. This function is accomplished by, for example, software.

[0036] The D/A converting unit 11 converts a digital signal outputted from the adding unit 17 into an analog signal, and outputs it.

[0037] Whereby, in the signal amplitude change processing unit 10, with respect to an inputted signal (signal to be transmitted), a power value of odd order of an amplitude quantity contained in a signal to be inputted to the amplifying unit 12 is calculated by the odd-order component calculating unit 14. In the multiplying unit 16, coefficient information is weighed on the calculated value and an attenuation signal having the inverted phase is outputted therefrom. Further, the weighed value is added to the input signal in the adding unit 17, and the input signal is outputted as a processed signal. The processed signal is inputted to the amplifying unit 12, then transmitted.

[0038] Namely, distortion components of powers of an input signal are determined in calculation, the obtained values are phase-inverted and multiplied by the coefficient information, the results are added to the input signal, then inputted to the amplifier 12. Whereby, power components of odd orders are removed from a signal to be inputted to the amplifier 12, so which the adjacent channel power contained in an output signal is decreased.

[0039] The coefficient information is determined according to an average output power which the amplifier 12 will transmit. Frequency of reference to the coefficient information is a unit of time for switching the average output power, so which the coefficient table 15 is not required to operate at a high speed.

[0040] More details of the odd-order component calculating unit 14 are shown in FIG. 3. FIG. 3 is a block diagram showing in more detail the predistortion-type distortion compensation amplifying apparatus 9 according to the first embodiment of this invention. The odd-order component calculating unit 14 (refer to FIG. 2) comprises the third-power component calculating units 14a and 14b (refer to FIG. 3). The coefficient table 15 (refer to FIG. 2) comprises coefficient tables 15a and 15b (refer to FIG. 3). The multiplying unit 16 (refer to FIG. 2) comprises multiplying units 16a and 16b.

[0041] The adding unit 17, the D/A converting unit 11 and the amplifier 12 shown in FIG. 3 are identical to those described above, further descriptions of which are thus omitted.

[0042] A sub odd-order component calculating unit 27a extracts a power component of the third-power (third-order) component of the amplitude quantity contained in a signal to be transmitted, and outputs it. The sub odd-order component calculating unit 27a comprises the third-power component calculating unit 14a, the coefficient table 15a, and

the multiplying unit 16a. The third-power component calculating unit 14a extracts a third-power component of the amplitude quantity contained in a signal to be transmitted. The coefficient table 15a can select one of plural pieces of coefficient information according to a signal from the outside, and output it. The multiplying unit 16a multiplies a signal outputted from the third-power component calculating unit 14a by coefficient information from the coefficient table 15a, and outputs an attenuation signal.

[0043] Similarly, the sub odd-order component calculating unit 27a extracts a power component of the fifth-power (fifth-order) component of the amplitude quantity included in a signal to be transmitted, and outputs it. The sub odd-order component calculating unit 27a comprises a fifth-power component calculating unit 14b, the coefficient table 15b, and the multiplying unit 16b. The fifth-power component calculating unit 14b extracts a fifth-power component of the amplitude quantity contained in a signal to be transmitted. The coefficient table 15b can select one of plural pieces of coefficient information according to a signal from the outside, and output it. The multiplying unit 16b multiplies a signal outputted from the fifth-power component calculating unit 14b by coefficient information from the coefficient table 15b, and outputs an attenuation signal.

[0044] Functions of the sub odd-order component calculating units 27a and 27b are accomplished by, for example, software. The coefficient tables 15a and 15b are substantially regions in the above coefficient table 15.

[0045] Although not shown, in the odd-order component calculating unit 14 in the signal amplitude change processing unit 10 shown in FIG. 3, it is possible to additionally provide a sub odd-order component calculating unit for the seventh power (not shown), a sub odd-order component calculating unit for the ninth power (not shown), and so on. A reason of this is to calculate a component of higher order such as the seventh power, the ninth power or the like, thereby increasing the accuracy. In such case, the sub odd-order component calculating unit for the seventh power comprises a seventh-power component calculating unit, a coefficient table for the seventh power, and a multiplying unit for the seventh power. Similarly, the sub odd-order component calculating unit for the ninth power comprises a ninth-power component calculating unit, a coefficient table for the ninth power, and a multiplying unit for the ninth power. The seventh-power component calculating unit and the ninth-power component calculating unit have the same function as which of the above third-power component calculating unit 14a, whereas the coefficient table for the seventh power and the coefficient table for the ninth power have the same function as which of the above coefficient table 15a, further descriptions of which are thus omitted. Similarly, the multiplying unit for the seventh power and the multiplying unit for the ninth power are identical to the above multiplying unit 16a, further descriptions of which are thus omitted.

[0046] Next, a flow of a signal inside the predistortion-type distortion compensation amplifying apparatus 9 will be described using Equations (7) to (11). Assuming which an amplitude function f of the amplifier 12 is expressed by Equation (7). Wherein, $M_i(t)$ is magnitude of amplitude of a modulated wave (corresponding to an amplitude quantity x of a signal to be transmitted), G is a gain of the amplifier 12, and A_n is coefficient information of the n -th power. Incidentally, $\sum_{n=1}^{\infty}$ represents a sum of 1 to infinity with respect to n .

$$f[M_i(t)] = G(\sum_{n=1}^{\infty} A_n \cdot M_i(t)^n) \quad (7)$$

[0047] When phase distortion is small, adjacent channel power ACP is expressed by Equation (8):

$$ACP = P_i(t) \cdot \sum_{n=1}^{\infty} B_n[M_i(t)] \quad (8)$$

where, $P_i(t)$ is a signal waveform inputted to the amplifier 12, and $B_n[M_i(t)]$ is the n -th coefficient information. $B_n[M_i(t)]$ is a function of magnitude $M_i(t)$ of amplitude of an inputted modulated wave, corresponding to an amplitude quantity x of a signal to be transmitted. A value of $B_n[M_i(t)]$ can be analytically determined using A_n when the modulation system is determined. For example, a value of $B_n[M_i(t)]$ is determined by Formula (9) in the case of QPSK (Quadrature Phase Shift Keying or Quadri Phase Shift Keying);

$$ACP = [(3/4)A_3x^2 + (1/2)A_5x^4 + \dots]P_i(t) \quad (9)$$

where A_3 and A_5 are the third-order and the fifth-order coefficient information of x . In practice, A_3 and A_5 become complex numbers including phase information since there is phase distortion. Further, these values are determined according to an average output power. In the case of the amplifier 12 having a constant output, these values are practically sufficient.

[0048] Accordingly, by beforehand subtracting this value from an input signal, it is possible to decrease the adjacent channel power. Namely, terms of the third order and the fifth order are weighed by coefficient information in the sub

odd-order component calculating units 27a and 27b (refer to FIG. 3), respectively. Deformed (undergone the predistortion process) I_{PD} and Q_{PD} signals are expressed by Equations (10) and (11), respectively:

$$I_{PD} = \{1 - (3/4)A_3x^2 + (1/2)A_5x^4 - \dots\} \cdot I \quad (10)$$

$$Q_{PD} = \{1 - (3/4)A_3x^2 + (1/2)A_5x^4 - \dots\} \cdot Q \quad (11)$$

[0049] Namley, as seen from these Equations (10) and (11), contribution of power components of odd orders of the amplitude quantity becomes small.

[0050] With the above structure, a flow of a signal inputted to the predistortion-type distortion compensation amplifying apparatus 9 is as follows.

[0051] In the third-power component calculating unit 14a in the sub odd-order component calculating unit 27a, a third-power component $A_3 \cdot x^3$ of an amplitude quantity x contained in a signal to be transmitted is extracted. In the multiplying unit 16a, coefficient information outputted from the coefficient table 15a and a signal outputted from the third-power component calculating unit 14a are multiplied, and an attenuation signal is outputted. Similarly, in the fifth-power component calculating unit 14b in the sub odd-order component calculating unit 27b, a fifth-power component $A_5 \cdot x^5$ of the amplitude quantity x of the signal to be transmitted is extracted. In the multiplying unit 16b, coefficient information outputted from the coefficient table 15b and a signal outputted from the fifth-power component calculating unit 14b are multiplied, and an attenuation signal is outputted. In the adding unit 17, the attenuation signals outputted from the sub odd-order component calculating units 27a and 27b are subtracted from the original signal to be transmitted, whereby the signal is outputted such which contribution of the third-power and the fifth-power components becomes small. Further, in the D/A converting unit 11, a digital signal outputted from the adding unit 17 is converted

into an analog signal, power-amplified in the amplifier 12, then transmitted from the antenna 13 (refer to FIG. 1). [0052] As this, distortion components of odd powers of a signal which are a problem in a spurious signal are determined in calculation, a phase of the signal is inverted, coefficient information is multiplied and added to the input signal, then inputted to the amplifier 12, whereby the adjacent channel power of the amplifier 12 is decreased. Therefore, an external RF circuit having a linear characteristic becomes unnecessary, and the circuit scale can be decreased, which contributes to a reduction of the power consumption or allows compactness of a portable telephone or the like.

[0053] When the output power is changed, coefficient information according to an output power which is a target is referred by memories of the coefficient tables 15a and 15b, and weighed. Since frequency of the reference is a unit of time for switching the average output power, intervals to refer to the memories have no relation with a speed of the signal. It is therefore possible to form the circuit with memories having a relatively slow access speed.

(A1) Description of First Modification of First Embodiment of the Invention

[0054] The structure of the above signal processing unit 8 may be another. Hereinafter, modifications of the above signal processing unit 8 will be described in first to third modifications.

[0055] FIG. 4 is a block diagram of a predistortion-type distortion compensation amplifying apparatus 9a according to a first modification of the first embodiment of this invention. The predistortion-type distortion compensation amplifying apparatus 9a shown in FIG. 4 comprises a signal processing unit 8a and an amplifier 12. The signal processing unit 8a comprises a signal amplitude change processing unit 10 and a control unit 20.

[0056] The signal amplitude change processing unit 10 outputs a signal such as to decrease contribution of power components of odd orders of an amplitude quantity contained in a signal to be transmitted, as described above. The signal amplitude change processing unit 10 comprises an odd-order component calculating unit 14, a coefficient table 15, a multiplying unit 16, an adding unit 17 and a D/A converting unit 11. Components of odd orders of the amplitude quantity are extracted from a signal to be transmitted in the odd-order component calculating unit 14, signals calculated from the values are outputted, and an attenuation signal obtained by multiplying the output signals by coefficient information having an inverted phase of the output signal is outputted. Incidentally, the coefficient table 15, the multiplying unit 16, the adding unit 17 and the D/A converting unit 11 other than the odd-order component calculating unit 14 are identical to those described above, further descriptions of which are thus omitted. The amplifier 12 is also identical to which described above, further description of which is thus omitted.

[0057] The control unit 20 variably adjusts a magnitude of the processed signal and inputs it to the amplifier 12, besides being operable to input a first address signal for selecting coefficient information for power components of odd orders to the signal amplitude change processing unit 10. The control unit 20 comprises a variable attenuating unit 19 and an output power control unit 18.

[0058] The variable attenuating unit 19 can variably adjust magnitude of the processed signal by the output power control unit 18, and input it to the amplifier 12. This function is accomplished by, for example, a variable resistor.

[0059] The output power control unit 18 can control the variable attenuating unit 19, besides being operable to input the first address signal for selecting coefficient information to the signal amplitude change processing unit 10. This function is accomplished by, for example, software. The output power control unit 18 inputs the first address signal to the coefficient table 15 in the signal amplitude change processing unit 10 at an appropriate timing from a main control unit (not shown), whereby appropriate coefficient information is outputted from the coefficient table 15. Incidentally, this coefficient information is determined according to an average output power which the amplifier 12 will transmit, so which the coefficient table 15 is not required to operate at a high speed like sampling intervals.

[0060] The signal amplitude change processing unit 10 is shown in more detail in FIG. 5. FIG. 5 is a block diagram showing in more detail the predistortion-type distortion compensation amplifying apparatus 9a according to the first modification of the first embodiment of this invention. The predistortion-type distortion compensation amplifying apparatus 9a shown in FIG. 5 comprises the signal processing unit 8a and the amplifier 12. The signal amplitude change processing unit 10 in the signal processing unit 8a comprises sub odd-order component calculating units 27a and 27b, an adding unit 17, and a D/A converting unit 11.

[0061] The sub odd-order component calculating unit 27a comprises a third-power component calculating unit 14a, a coefficient table 15a and a multiplying unit 16a. The sub odd-order component calculating unit 27b comprises a fifth-power component calculating unit 14b, a coefficient table 15b and a multiplying unit 16b. These are identical to those described above, further descriptions of which are thus omitted.

[0062] In order to increase the accuracy by calculating a component of higher order such as the seventh power, the ninth power or the like, it is possible to additionally provide a sub odd-order component calculating unit for the seventh power (not shown), a sub odd-order component calculating unit for the ninth-power (not shown) and so on in the signal amplitude change processing unit 10 shown in FIG. 5. In such case, the sub odd-order component calculating unit for the seventh power comprises a seventh-power component calculating unit, a coefficient table for the seventh power and a multiplying unit for the seventh power, whereas the sub odd-order component calculating unit for the ninth power comprises a ninth-power component calculating unit, a coefficient table for the ninth power and a multiplying unit for the ninth power. The seventh-power component calculating unit and the ninth-power component calculating unit have the same function as which of the above third-power component calculating unit 14a, whereas the coefficient table for the seventh power and the coefficient table for the ninth power have the same function as which of the above coefficient table 15a, further descriptions of which are thus omitted. The multiplying unit for the seventh power and the multiplying unit for the ninth power are identical to the above multiplying unit 16a, further descriptions of which are thus omitted.

[0063] In FIG. 5, like reference characters designate like or corresponding parts having the same or similar functions, further descriptions of which are thus omitted.

[0064] A value of a power component of the third order of the amplitude quantity contained in a signal to be transmitted which has been inputted to the signal amplitude change processing unit 10 is calculated by the third-power component calculating unit 14a in the sub odd-order component calculating unit 27a, and a signal obtained by multiplying the calculated value by coefficient information is outputted from the multiplying unit 16a. Similarly, a value of a power component of the fifth order the amplitude quantity contained in the signal is calculated by the fifth-power component calculating unit 14b in the sub odd-order component calculating unit 27b, and a signal obtained by multiplying the calculated value by coefficient information is outputted from the multiplying unit 16b. Weighted signals outputted from the multiplying units 16a and 16b and the input signal are added in the adding unit 17, and outputted as a processed signal. The processed signal is converted from digital to analog in the D/A converting unit 11. The signal is then adjusted to appropriate magnitude in the variable attenuating unit 19 in the control unit 20, inputted to the amplifying unit 12, then transmitted from the antenna 13 (refer to FIG. 1).

[0065] With the above structure, contribution of power components of odd orders of the amplitude quantity contained in a signal to be transmitted is decreased, and magnitude of the output power at the amplifier 12 is controlled. Namely, distortion power components of a signal to be transmitted are determined in calculation, and signals obtained by multiplying the components by coefficient information having the inverse phase are subtracted from an input signal.

[0066] Magnitude of the output power is adjusted by a control signal outputted from the output power control unit 18 according to an instruction from a main control unit (not shown). The processed signal is adjusted to appropriated magnitude in the variable attenuating unit 19, and inputted to the amplifier 12. When the magnitude of the output power is changed, coefficient information according to an output power which is a target is referred by the coefficient table 15 (or 15a, or 15b), and weighed.

[0067] As this, the adjacent channel power which is a problem in a spurious signal is decreased. Accordingly, an external RF circuit having a linear characteristic becomes unnecessary, and the circuit scale can be decreased. It is therefore possible to contribute to a reduction of the power consumption, and allows compactness of a portable telephone or the like. Since frequency of reference to the coefficient table 15 (or 15a, or 15b) is a unit of time for switching the average output power, it is possible to form the circuit with a memory having a relatively slow access speed.

(A2) Description of Second Modification of First Embodiment of the Invention

[0068] FIG. 6 is a block diagram of a predistortion-type distortion compensation amplifying apparatus 9b according to a second modification of the first embodiment of this invention. The predistortion-type distortion compensation amplifying apparatus 9b shown in FIG. 6 comprises a signal processing unit 8b and an amplifier 12. A detecting unit 22 is disposed on the output's side of the amplifier 12. The signal processing unit 8b comprises a signal amplitude change processing unit 10 and a control unit 20, along with an amplitude information comparing unit 21 and an A/D converter 23.

[0069] The signal amplitude change processing unit 10 outputs a processed signal such as to decrease contribution of power components of odd orders of an amplitude quantity contained in a signal to be transmitted, as stated above. The signal amplitude change processing unit 10 comprises an odd-order component calculating unit 14, a coefficient table 15, a multiplying unit 16, an adding unit 17 and a D/A converting unit 11. These are identical to those described above, further descriptions of which are thus omitted. Components of odd orders of the amplitude quantity are extracted from a signal to be transmitted in the odd-order component calculating unit 14, signals calculated from the values are outputted, then an attenuation signal obtained by multiplying coefficient information having an inverted phase of the output signal is outputted.

[0070] The control unit 20 can variably adjust magnitude of the processed signal and input it to the amplifier 12, besides being operable to input a first address signal for selecting coefficient information for power components of odd orders to the signal amplitude change processing unit 10, as stated above. The control unit 20 comprises a variable attenuating unit 19 and an output power control unit 18. These functions are identical to those described above, further descriptions of which are thus omitted. The output power control unit 18 inputs the first address signal at an appropriate timing from a main control unit (not shown) to the coefficient table 15 in the signal amplitude change processing unit 10, whereby appropriate coefficient information is outputted from the coefficient table 15. Incidentally, the coefficient information is determined according to an average output power which the amplifier 12 will transmit. The coefficient table 15 is not necessarily required to operate at a high speed.

[0071] The A/D converter 23 converts an analog signal to a digital signal.

[0072] The amplitude information comparing unit 21 can input a first select signal generated based on a signal to be transmitted and a signal outputted from the amplifier 12 to the signal amplitude change processing unit 10. The amplitude information comparing unit 21 comprises an input amplitude calculating unit 21a, an output amplitude calculating unit 21c and a comparing-rewriting unit 21b.

[0073] The input amplitude calculating unit 21a extracts an amplitude quantity from a signal to be transmitted, and outputs input amplitude information. The output amplitude calculating unit 21c extracts an amplitude quantity from a signal outputted from the amplifier 12, and outputs output amplitude information.

[0074] The comparing-rewriting unit 21b can input the first select signal to the signal amplitude change processing unit 10 according to an error between the output amplitude information and the input amplitude information. When the input amplitude information is smaller than the output amplitude information, the comparing-rewriting unit 21b inputs the first select signal to the signal amplitude change processing unit 10 in order to rewrite the coefficient information in the coefficient table 15 such which the coefficient information becomes larger. When the input amplitude information is larger than the output amplitude information, the comparing-rewriting unit 21b inputs the first select signal to the signal amplitude change processing unit 10 in order to rewrite the coefficient information in the coefficient table 15 such which the coefficient information becomes smaller. Whereby, an error of the memory is corrected.

[0075] The detecting unit 22 monitors magnitude of an output signal of the amplifier 12, decreasing a gain of the signal amplified by the amplifier 12 and outputting it. This function is accomplished using a directional coupler 22a. The directional coupler 22a can drop a gain of the signal amplified by the amplifier 12 by, for example, about 20dB, and output it. To the directional coupler 22a, an output terminator 22b of 50Ω is connected. Although the detecting unit 22 uses the directional coupler 22a, the present invention is not limited to this example, but another detecting method may be employed.

[0076] A signal outputted from the detecting unit 22 is converted from analog to digital in the A/D converter 23, inputted to the output amplitude calculating unit 21c, at which magnitude of the output signal is monitored. Namely, a part of the input signal is branched, a part of the output of the amplifier 12 is branched, then the part of the input and the part of the output are compared in the comparing-rewriting unit 21b. When an error between them is large, the first select signal is outputted, whereby the coefficient table 15 is rewritten. It is thereby possible to cope with a change in characteristics of the amplifier 12 due to a change in temperature or a change with time. Object here is to correct an error of the memory, a longer time interval than a sampling time for the signal suffices to access to the coefficient table 15.

[0077] The signal amplitude change processing unit 10 is shown in more detail in FIG. 7. FIG. 7 is a block diagram showing in more detail the predistortion-type distortion compensation amplifying apparatus 9b according to the second modification of the first embodiment of this invention. The signal amplitude change processing unit 10 in the signal processing unit 8b comprises sub odd-order component calculating units 27a and 27b along with the adding unit 17

and the D/A converting unit 11.

[0078] The sub odd-order component calculating unit 27a comprises a third-power component calculating unit 14a, a coefficient table 15a and a multiplying unit 16a. The sub odd-order component calculating unit 27b comprises a fifth-power component calculating unit 14b, a coefficient table 15b and a multiplying unit 16b. These are identical to those described above, further descriptions of which are thus omitted.

[0079] In order to increase the accuracy by calculating a component of higher order such as the seventh power, the ninth power, or the like, the signal amplitude change processing unit 10 shown in FIG. 7 may additionally have a sub odd-order component calculating unit for the seventh power (not shown), a sub odd-order component calculating unit for the ninth power (not shown), and so on. In such case, the sub odd-order component calculating unit for the seventh power comprises a seventh-power component calculating unit, a coefficient table for the seventh power and a multiplying unit for the seventh power, whereas the sub odd-order component calculating unit for the ninth power comprises a ninth-power component calculating unit, a coefficient table for the ninth power and a multiplying unit for the ninth power. The seventh-power component calculating unit and the ninth-power component calculating unit have the same function as which of the above third-power component calculating unit 14a, whereas the coefficient table for the seventh power and the coefficient table for the ninth power have the same function as which of the above coefficient table 15a, further descriptions of which are thus omitted. The multiplying unit for the seventh power and the multiplying unit for the ninth power are identical to the above multiplying unit 16a, further descriptions of which are thus omitted.

[0080] In FIG. 7, like reference characters designate parts being identical to or having identical functions to those described above, further descriptions of which are thus omitted.

[0081] In the signal amplitude change processing unit 10, values of the third power and the fifth power of an amplitude quantity contained in a transmit signal to be inputted to the amplifier 12 are calculated, and the calculated values are multiplied by coefficient information, thus weighed. The weighed values are added to an input signal in the adding unit 17, whereby a processed signal is obtained. In the variable attenuating unit 19 in the control unit 20, the processed signal is attenuated, inputted to the amplifier 12, and transmitted.

[0082] In the output amplitude calculating unit 21c, magnitude of an amplitude quantity of an output signal from the amplifier 12 is calculated, and output amplitude information is outputted. In the input amplitude calculating unit 21a, magnitude of an amplitude quantity of a signal to be transmitted is extracted, and input amplitude information is outputted. In the comparing-rewriting unit 21b, the output amplitude information and the input amplitude information are compared. When the input amplitude information is smaller than the output amplitude information, the comparing-rewriting unit 21b inputs a first select signal according to an error between the output amplitude information and the input amplitude information in order to rewrite the coefficient information in the coefficient table 15 (or 15a, or 15b) in the signal amplitude change processing unit 10 such which the coefficient information becomes larger. When the input amplitude information is larger than the output amplitude information, the comparing-rewriting unit 21b inputs the first select signal in order to rewrite the coefficient information in the coefficient table 15 (or 15a, or 15b) in the signal amplitude change processing unit 10 such which the coefficient information becomes smaller. As shown in FIG. 7, the first select signal is inputted to the coefficient tables 15a and 15b for respective orders.

[0083] With the above structure, distortion components of odd powers of a signal are determined in calculation, a phase of the components is inverted, coefficient information is multiplied, results are added to an input signal, and the input signal is inputted to the amplifier 12. Whereby, contribution of power components of odd orders of the amplitude quantity contained in a signal to be transmitted is decreased.

[0084] On the other hand, magnitude of an output power in the amplifier 12 is controlled. The magnitude of the output power is adjusted by a control signal outputted from the output power control unit 18 according to an instruction from a main control unit (not shown). Namely, the processed signal is adjusted to appropriate magnitude in the variable attenuating unit 19, and inputted to the amplifier 12. And the more, when the magnitude of the output power is changed, coefficient information according to an output power which is a target is referred by the coefficient table 15 (or 15a, or 15b), and weighed.

[0085] A part of the input and a part of the output of the amplifier 12 are compared. The coefficient table 15 (or 15a, or 15b) is such rewritten which the coefficient information becomes larger when the input is smaller, or which the coefficient information becomes smaller when the input is larger. A value to be outputted is thereby corrected.

[0086] As this, the adjacent channel power which is a problem in a spurious signal is decreased in the transmit channel. Further, it becomes possible to form a distortion compensation circuit which does not require an external RF circuit, which contributes to a reduction of the power consumption and allows compactness of a portable telephone or the like.

[0087] By comparing magnitudes of the amplitude quantities of the input and output signals, an error of the memory is corrected, so which an access to the coefficient table 15 (or 15a, or 15b) is at a longer interval than the sampling time for the signal. It is thus possible to form the circuit with a memory having a relatively slow access speed.

(A3) Description of Third Modification of First Embodiment of the Invention

[0088] FIG 8 is a block diagram of a predistortion-type distortion compensation amplifying apparatus 9c shown in FIG. 8 according to a third modification of the first embodiment of this invention. The predistortion-type distortion compensation amplifying apparatus 9c comprises a signal processing unit 8c and an amplifier 12. A detecting unit 22 is provided on the output's side of the amplifier 12. The signal processing unit 8c comprises a signal amplitude change processing unit 10a, a control unit 20, an amplitude information comparing unit 21, a differential/integral coefficient information adding unit 26 and an A/D converter 23.

[0089] The signal amplitude change processing unit 10a can output a processed signal such which contribution of power components of odd orders of an amplitude quantity contained in a signal to be transmitted is decreased. The signal amplitude change processing unit 10a comprises an odd-order component calculating unit 14, a coefficient table 15, a multiplying unit 16, an adding unit 17, and a D/A converting unit 11. The signal amplitude change processing unit 10a additionally comprises a multiplying unit 25 disposed on the output's side of the adding unit 17. The multiplying unit 25 multiplies a signal outputted from the adding unit 17 by second coefficient information outputted from the differential/integral coefficient information adding unit 26, and outputs a result as a processed signal. The odd-numbered degree component calculating unit 14 is shown in more detail in FIG. 9.

[0090] FIG. 9 is a block diagram showing in more detail the predistortion-type distortion compensation amplifying apparatus 9c according to the third modification of the first embodiment of this invention. The signal processing unit 10a shown in FIG. 9 comprises sub odd-order component calculating units 27a and 27b, along with the adding unit 17, the D/A converting unit 11 and the multiplying unit 25. The adding unit 17, the D/A converter 11 and the multiplying unit 25 are identical to those described above, further descriptions of which are thus omitted.

[0091] In order to increase the accuracy of the operation, the signal amplitude change processing unit 10a may additionally comprise a sub odd-order component calculating unit for the seventh power (not shown), and a sub odd-order component calculating unit for the ninth power (not shown). In such case, the sub odd-order component calculating unit for the seventh power comprises a seventh-power component calculating unit, a coefficient table for the seventh power and a multiplying unit for the seventh power, whereas the sub odd-order component calculating unit for the ninth power comprises a ninth-power component calculating unit, a coefficient table for the ninth power and a multiplying unit for the ninth power. The seventh-power component calculating unit and the ninth-power component calculating unit have the same function as which of the above third-power component calculating unit 14a, whereas the coefficient table for the seventh power and the coefficient table for the ninth power have the same function as which of the above coefficient table 15a, further descriptions of which are thus omitted. The multiplying unit for the seventh power and the multiplying unit for the ninth power are identical to the above multiplying unit 16a, further descriptions of which are thus omitted.

[0092] Namely, in the signal amplitude change processing unit 10a, a signal outputted from the third-order component calculating unit 14a is multiplied by coefficient information having the inverted phase in the multiplying unit 16a, whereas a signal outputted from the fifth-order component calculating unit 14b is multiplied by coefficient information having the inverted phase in the multiplying unit 16b. Outputs from the multipliers 16a and 16b are operated in the adding unit 17 of the signal amplitude change processing unit 10a, and an attenuation signal is outputted. The attenuation signal is multiplied by the second coefficient information inputted from the differential/integral coefficient information adding unit 26, and a processed signal is outputted.

[0093] On the other hand, the differential/integral coefficient information adding unit 26 can calculate first differential/integral information based on an amplitude quantity of a signal to be transmitted, calculate second differential/integral information based on an amplitude quantity of a signal outputted from the amplifier 12, and input the second coefficient information obtained based on the above first differential/integral information and the above second differential/integral information to the signal amplitude change processing unit 10a.

[0094] The differential/integral information is a differential coefficient such as a difference value in magnitude of amplitude quantities of signals, an integral value of an amplitude quantity of a signal within a constant time, a combination thereof, or the like.

[0095] The second coefficient information signifies coefficient information outputted from the differential/integral information adding unit 26, which can be used to compensate the frequency characteristic in the vicinity of a high frequency in a bias circuit (not shown) of the amplifier 12, or a bias circuit (not shown) in the power circuit, or the matching circuit. Incidentally, the second coefficient information is discriminated from coefficient information outputted from the above coefficient table 15 (or 15a, or 15b). The above is the same in the following description.

[0096] The differential/integral information adding unit 26 comprises a first differential/integral information calculating unit 26a, a second differential/integral information calculating unit 26d, a second coefficient table (second coefficient information outputting unit) 26c, and a second comparing-rewriting unit 26b.

[0097] The first differential/integral information calculating unit 26a can calculate first differential/integral information based on an amplitude quantity of a signal to be transmitted and output it, besides being operable to input a second

address signal obtained based on the amplitude quantity of the signal to be transmitted to the second coefficient table 26c. The second differential/integral information calculating unit 26d calculates second differential/integral information based on an amplitude quantity of a signal outputted from the amplifier 12, and outputs it.

[0098] The second coefficient table 26c can select one of plural pieces of coefficient information according to the second address signal, and output it as second coefficient information to the signal amplitude change processing unit 10a.

[0099] The second comparing-rewriting unit 26b rewrites the second coefficient information according to an error between the above first differential/integral information and the above second differential/integral information. The second comparing-rewriting unit 26b compares first differential/integral information with second differential/integral information, and rewrites the second coefficient information such which the error therebetween becomes smaller. For example, from signals which are a part of a signal to be transmitted and a part of an output signal of the amplifier 12, absolute values of positive and negative differential coefficients of the second power of amplitude quantities of these signals are calculated, and the second coefficient information is rewritten such which an error therebetween is nullified. Incidentally, as this rewriting method, other than the above may be employed.

[0100] A differential coefficient or an integral coefficient of magnitude of an amplitude quantity of an input signal is calculated in the first differential/integral information calculating unit 26a in the differential/integral information adding unit 26. The second coefficient information determined from the magnitude of the differential coefficient or the integral coefficient is inputted from the second coefficient table 26c to the multiplier 25 in the signal amplitude change processing unit 10a. In the multiplying unit 25, an attenuation signal at the output of the adding unit 17 and the second coefficient information are multiplied, and outputted as a processed signal. The processed signal can be used to compensate a frequency characteristic in the vicinity of a high frequency in a bias circuit (not shown) of the amplifier 12, a bias circuit (not shown) in the power circuit, or the matching circuit.

[0101] In FIGs. 8 and 9, like reference characters designate parts having the same or similar functions described above, further descriptions of which are thus omitted.

[0102] In the signal amplitude change processing unit 10a, power values of odd orders of an amplitude quantity contained in a transmit signal to be inputted to the amplifier 12 are calculated, and the calculated values are multiplied by coefficient information, thus weighed. The weighed values are added to an input signal, then the input signal is outputted as a processed signal from the adding unit 17. In the variable attenuating unit 19 in the control unit 20, the processed signal is attenuated, inputted to the amplifier 12, and transmitted.

[0103] The output power control unit 18 inputs a first address signal to the coefficient tables 15a and 15b in the signal amplitude change processing unit 10a at an appropriate timing from a main control unit (not shown), whereby appropriate coefficient information is outputted from the coefficient tables 15a and 15b. The coefficient information is determined according to an average output power which the amplifier 12 will transmit. The coefficient tables 15a and 15b are not necessarily required to operate at a high speed.

[0104] A part of the input which has been branched is inputted to the first differential/integral information calculating unit 26a. The second coefficient information derived from the calculated value is inputted from the second coefficient table 26c to the signal amplitude change processing unit 10a. In the multiplying unit 25, a processed signal from the adding unit 17 and the second coefficient information are multiplied, and a result of it is outputted through the D/A converting unit 11. The result is adjusted to appropriate magnitude in the variable attenuating unit 19, inputted to the amplifier 12, and transmitted from the antenna 13 (refer to FIG. 1).

[0105] On the other hand, the gain of a part of an output signal of the amplifier 12 is decreased in the detecting unit 22, and the part of the output signal is inputted to the amplitude information comparing unit 21 and the differential/integral coefficient information adding unit 26 via the A/D converting unit 23. In the amplitude information comparing unit 21, a part of the input and a part of the output are compared. When an error between them is large, coefficient information in the coefficient tables 15a and 15b are rewritten.

[0106] In the differential/integral coefficient information adding unit 26, absolute values of positive and negative differential coefficients of the second power are calculated from a part of the input and a part of the output of the amplifier 12. The second coefficient information in the second coefficient table 26c is such rewritten which a difference between them is nullified.

[0107] With the above structure, contribution of power components of odd orders of an amplitude quantity contained in a signal to be transmitted is decreased, and magnitude of an output power in the amplifier 12 is controlled. Namely, distortion components of powers of a signal to be transmitted are determined in calculation, and signals obtained by multiplying coefficient information having the inverted phase are subtracted from an input signal. The subtracted value is corrected according to an error detected from the input signal and an output signal, then inputted to the amplifier 12.

[0108] The output power is adjusted to appropriate magnitude by a control signal outputted from the output power control unit 18 according to an instruction from a main control unit (not shown), and inputted to the amplifier 12. When the output power is changed, coefficient information according to an output power which is a target is referred by the coefficient tables 15a and 15b, and weighed.

[0109] Further, a part of the input and a part of the output of the amplifier 12 are compared. Values outputted from the coefficient tables 15a and 15b are such corrected which the coefficient information becomes larger when the input is smaller, or which the coefficient information becomes smaller when the input is larger.

[0110] As above, the adjacent channel power of the transmit channel which is a problem in a spurious signal is decreased. Additionally, it becomes possible to form a distortion compensation circuit which does not require an external RF circuit, which contributes to a reduction of the power consumption, and allows compactness of a portable telephone or the like.

[0111] Since the coefficient tables 15a and 15b, and the second coefficient table 26c are rewritten as above and an error of the memory is thereby corrected, it is possible to cope with a change in characteristics of the amplifier 12 due to a change in temperature or a change with time. Since a longer time interval than a sampling time for a signal suffices to access to the coefficient tables 15a and 15b, and the second coefficient table 26c, it is possible to form the circuit with a memory having a relatively slow access speed.

[0112] The differential/integral coefficient information adding unit 26 compares a value obtained by using a differential value, an integral value, or the both values of an input signal with a value obtained by using a differential value, an integral value, or the both values of an output signal, and such rewrites the second coefficient table 26c which an error therebetween becomes smaller. It is thereby possible to cope with a change in characteristics of the amplifier 12 due to a change in temperature or a change with time.

[0113] The frequency characteristic in the vicinity of a high frequency of a bias circuit (not shown) of the amplifier 12, a bias circuit (not shown) of the power circuit or the matching circuit is compensated as above, so which the accuracy is improved. Additionally, it is possible to cope with a change in characteristics of the amplifier 12 due to a change in temperature or a change with time.

(B) Description of Second Embodiment of the Invention

[0114] FIG. 10 is a block diagram showing a transmitting unit of a radio transmitter to which a predistortion-type distortion compensation amplifying apparatus according to a second embodiment of this invention is applied. A radio transmitter 7b shown in FIG. 10 comprises a predistortion-type distortion compensation amplifying apparatus 9d and an antenna 13.

[0115] The predistortion-type distortion compensation amplifying apparatus 9d comprises a signal processing unit 30 and an amplifier 12. The signal processing unit 30 performs a process with a function representing a reverse characteristic of an input-output characteristic of the amplifier 12 on a signal to be transmitted, performs a coefficient changing process, whereby an odd-order component appears in an output signal of the amplifier 12. The amplifier 12 thus operates in a non-saturation region. Here, "the amplifier 12 operates in a non-saturation region" signifies which the amplifier 12 operates in a range of a non-saturation region of the input-output characteristic of the amplifier 12, details of which will be described later. The signal processing unit 30 comprises a non-linear processing unit 41, a predistortion processing unit 32 and a D/A converting unit 11.

[0116] The non-linear processing unit 41 performs a process with a function representing a reverse characteristic of the input-output characteristic of the amplifier 12, which has been beforehand obtained, on a signal to be transmitted, and outputs the signal. The predistortion processing unit 32 multiplies the output processed by the non-linear processing unit 41 by predetermined coefficient information, and outputs a result as a processed signal. In the non-linear processing unit 41 and the predistortion processing unit 32, the digital processing is performed. The D/A converting unit 11 converts a digital signal to an analog signal. Incidentally, the amplifier 12 and the antenna 13 are identical to those described above, further descriptions of which are thus omitted.

[0117] FIG. 11 is a block diagram of the radio transmitter according to the second embodiment of this invention. The predistortion processing unit 32 shown in FIG. 11 comprises a frequency component attenuating unit 33 which converts a signal outputted from the non-linear processing unit 41 into a frequency domain, attenuates a desired frequency component, reversely converts an attenuated signal into a time domain and outputs the converted signal, and a coefficient multiplying unit 34 (denoted as polynomial approximation in FIG. 11) which compares the converted signal outputted from the frequency component attenuating unit 33 with a signal to be transmitted, multiplies a power component of an odd-order of the amplitude quantity by desired coefficient information, and outputs a result as a processed signal.

[0118] The frequency attenuating unit 33 comprises a fast Fourier transform unit 33a which converts a signal outputted from the non-linear processing unit 41 into a frequency domain, a filter 33b which attenuates a desired frequency component and outputs it, and an inverse fast Fourier transform unit 33c which inversely converts the attenuated signal into a time domain and outputs it.

[0119] A method for determining coefficient information on a non-linear component in the predistortion processing unit 32 will be now described with reference to FIG. 11. First, deformation of an ideal input waveform is calculated with $y=Gx$, and an adjacent channel power thereof is made larger (refer to a frame denoted by A1). Next, the deformed

result is converted into a frequency domain using FFT (Fast Fourier Transformation) (refer to a frame denoted by A2), a part of the frequency at which the leakage power is desired to be decreased is filtered to be attenuated (refer to a frame denoted by A3), the result is restored to the time domain using the reverse FFT, the time waveform is compared with the input waveform, the input-output is approximated using a polynomial, whereby coefficient information is determined.

[0120] FIG. 12 is a diagram showing an example of the input-output characteristic of the amplifier 12. The horizontal axis in FIG. 12 represents input power (dB), whereas the vertical axis represents output power (dB). A part denoted by B1 is a non-saturation region, whereas a part denoted by B2 is a saturation region.

[0121] Meaning of "operating in the non-saturation region" is as follows. The input-output characteristic of the amplifier 12 shown in FIG. 12 is discontinuous at a point (refer to A) where the output changes such as a saturation region (refer to B2). In order to eliminate an effect of distortion of higher order at the discontinuous point in the case which the amplifier 12 is linearized, a non-linear component of the third, fifth, or odd power is made faintly remain when the amplifier 12 is linearized, whereby the input-output characteristic is optimized. The amplifier 12 thereby operates in the non-saturation region (refer to B1), so which a spurious signal is prevented from spreading in a wideband.

[0122] FIG. 13(a) is a diagram showing a signal waveform having an envelope composed of a fundamental wave and a third-order component. As shown in FIG. 13(a), if the third component signal is added to signals of two waves having an equal amplitude quantity, it is possible to create a signal having an output level which does not exceed the saturation when it is amplified by the amplifier 12. Since the third-order component is created by raising the signal to the third power, the signal cubed is multiplied by appropriate coefficient information, and the result is added to the original signal.

[0123] FIG. 13(b) is a diagram showing a signal spectrum of an output waveform in the case where the signal waveform shown in FIG. 13(a) is amplified by the amplifier 12 having the input-output characteristic shown in FIG. 12. As shown in FIG. 13(b), the signal spectrum does not overflow the saturation region, thus spurious components in a wideband do not generate. Namely, by such giving an optimized input-output characteristic which the third-power non-linear component remains to the amplifier 12, the spurious components are prevented from spreading in a wideband.

[0124] FIG. 14(a) is a diagram showing a signal waveform having an envelope composed of a fundamental wave, and third-order and fifth-order components. As shown in FIG. 14(a), if third-order and fifth-order component signals are added to signals of two waves having an equal amplitude quantity, it is possible to create a signal whose output level does not exceed the saturation region when the signal is amplified by the amplifier 12. Since the third-order component is generated by raising the signal to the third power whereas the fifth component is generated by raising the signal to the fifth power, the third-power and the fifth-power of the signal are multiplied by appropriate coefficient information, and the results are added to the original signal.

[0125] FIG. 14(b) is a diagram showing a signal spectrum having an output waveform in the case where the signal waveform shown in FIG. 14(a) is amplified by the amplifier 12 having the input-output characteristic shown in FIG. 12. As shown in FIG. 14(b), spurious components in a wideband do not generate. Namely, by such optimizing the input-output characteristic of the amplifier 12 which the third-power and fifth-power non-linear components remain, it does not exceed the saturation region.

[0126] In FIG. 11, an inputted baseband signal is undergone a process with a function representing an inverted characteristic of the input-output characteristic of the amplifier 12 which has been beforehand obtained in the non-linear processing unit 41, and outputted. The output signal is filtered in the frequency component attenuating unit 33, and outputted. In the coefficient multiplying unit 34, the inputted baseband signal and a signal outputted from the frequency component attenuating unit 33 are compared, a power component of odd order of the amplitude quantity is multiplied by desired coefficient, and the result is outputted as a processed signal through the D/A converting unit 11. In the amplifier 12, the processed signal is power-amplified, then radio-transmitted from the antenna 13 (refer to FIG. 10).

[0127] With the above structure, the amplifier 12 is used in such a mode which the amplifier 12 operates in a non-saturation region according to this embodiment. As an example, this will be described with reference to FIGs. 15 and 16 by way of examples in PDC (Personal Digital Cellular Telecommunication System) and CDMA (Code Division Multiple Access).

[0128] An example where PDC is applied to the portable telephone standard is as follows. Namely, the input-output characteristic (refer to FIG. 12) is deformed as Equation (12):

$$y = G(x - 9.5858 \times 10^{-2} x^3 + 6.2532 \times 10^{-3} x^5 - 2.0519 \times 10^{-4} x^7 + 2.5791 \times 10^{-6} x^9) \quad (12)$$

where, y is magnitude of a modulated wave component of the output, and G is a gain of the amplifier 12.

[0129] FIG. 15 shows a result of comparison between general linearization and linearization according to this em-

bodiment in PDC. The horizontal axis in FIG. 15 represents output power of the amplifier 12, whereas the vertical axis represents adjacent channel power of the output power. A curved line marked with cross signs shown in FIG. 15 represents normal deformation with $y=Gx$, whereas a curved line marked with circle signs represents deformation with Equation (12). The curve marked with the circle signs can more decrease the adjacent channel power than the curve marked with the cross signs.

[0130] A case of a communication system in which frequency channels are in close proximity like CDMA is as expressed by Equation (13). Incidentally, the input-output characteristic is as shown in FIG. 12.

$$y=G(x-3 \times 10^{-2}x^3) \quad (13)$$

[0131] FIG. 16 shows a result of comparison between normal linearization and linearization according to this embodiment in CDMA. As shown in FIG. 16, this embodiment can more decrease the adjacent channel power than the case where deformation by $y=Gx$ is performed. In FIG. 16, although ACP1 degrades, an output according to this embodiment satisfying both ACP1 and ACP2 is larger.

[0132] As above, by inputting a result obtained by adding a component of the third power, a component of the fifth power, or an addition of the component of the third power and the component of the fifth power to an input signal to the amplifier 12, the amplifier 12 can such operate as not to exceed the saturation region. Even if the input signal is amplified by the amplifier 12 having the input-output characteristic shown in FIG. 12, it is possible to prevent generation of a spurious signal in a wideband.

[0133] It is also possible to compensate distortion without an external RF circuit having a linear amplification characteristic. It is thus possible to decrease the circuit size, and contribute to a reduction of the power consumption and allows compactness of a portable telephone or the like.

(B1) Description of First Modification of Second Embodiment of the Invention

[0134] FIG. 17 is a block diagram showing a transmitting unit of a radio transmitter to which a predistortion-type distortion compensation amplifying apparatus according to a first modification of the second embodiment of this invention is applied. In a predistortion-type distortion compensation amplifying apparatus 9e shown in FIG. 17, the differential/integral coefficient information adding unit 26 is added to the predistortion-type distortion compensation amplifying apparatus 9d (refer to FIG. 10). Namely, the predistortion-type distortion compensation amplifying apparatus 9e comprises a signal processing unit 30, an amplifier 12 and the differential/integral coefficient information adding unit 26. A detecting unit 22 is connected to the output's side of the amplifier 12.

[0135] The signal processing unit 30 performs a process with a function representing a reverse characteristic of the input-output characteristic of the amplifier 12 on a signal to be transmitted, performs the coefficient changing process to make an odd-order component appear in an output signal of the amplifier 12, whereby the amplifier 12 operates in the non-saturation region. A non-linear processing unit 41, a predistortion processing unit 32 and a D/A converting unit 11 in the signal processing unit 30 are identical to those described in the first and second embodiments, further descriptions of which are thus omitted. Additionally, the amplifier 12 and the detecting unit 22 are identical to those described above, further descriptions of which are thus omitted, as well.

[0136] The differential/integral coefficient information adding unit 26 can calculate first differential/integral information from an amplitude quantity of a signal outputted from the amplifier 12, calculate second differential/integral information from an amplitude quantity of a signal to be transmitted, and input second coefficient information derived from the above first differential/integral information and the above second differential/integral information to the coefficient multiplying unit 34 in the signal processing unit 30. The differential/integral coefficient information adding unit 26 comprises a first differential/integral information calculating unit 26a, a second differential/integral information calculating unit 26d, a second coefficient table 26c and a second comparing-rewriting unit 26b.

[0137] The first differential/integral information calculating unit 26a can calculate the first differential/integral information from the amplitude quantity of the signal to be transmitted and output it, and input a second address signal obtained from the amplitude quantity of the signal to be transmitted to the second coefficient table 26c. The second differential/integral information calculating unit 26d calculates the second differential/integral information from an amplitude quantity of a signal outputted from the amplifier 12, and outputs it. The second coefficient table 26c can select one of plural pieces of coefficient information according to the second address signal, and output it as the second coefficient information to the signal processing unit 30.

[0138] The second comparing-rewriting unit 26b rewrites the second coefficient information according to an error between the above first differential/integral information and the above second differential/integral information. The second comparing-rewriting unit 26b compares the first differential/integral information with the second differential/

integral information, and such rewrites the second coefficient information which the error becomes smaller.

[0139] For example, absolute values of positive and negative differential coefficients of the second power of amplitude quantities of these which are a part of the signal to be transmitted and a part of the output signal of the amplifier 12 are calculated from these signals, the second coefficient information is such rewritten which an error therebetween is nullified. Another method may be employed as this rewriting method.

[0140] In FIG. 17, an inputted baseband signal is undergone a process with a function representing an inverted characteristic of the input-output characteristic of the amplifier 12 which has been beforehand obtained in the non-linear processing unit 41, and outputted. The output signal is filtered by the frequency component attenuating unit 33, and outputted. In the coefficient multiplying unit 34, the inputted baseband signal is compared with a signal outputted from the frequency component attenuating unit 33, a power component of odd order of the amplitude quantity is multiplied by desired coefficient information, and the result is outputted as a processed signal. The processed signal is converted from digital to analog in the D/A converting unit 11, power-amplified in the amplifier 12, and radio-transmitted from the antenna 13 through the detecting unit 22.

[0141] With the above structure, by inputting a result obtained by adding a component of the third-power term, a component of the fifth-power term, or both of the component of the third-power term and the component of the fifth-power term to an input signal to the amplifier 12, a non-linear component of the third power, the fifth power or odd power faintly remains on the output's side of the amplifier 12, whereby the amplifier 12 operates, not exceeding the saturation region (refer to FIGs. 13 and 14).

[0142] The second coefficient information is determined from a differential value/integral value of the amplitude quantity of the input signal, and the original signal is multiplied by the second coefficient information. The result is outputted as a predistortion signal. A differential value, or an integral value, or the both values of the input signal is compared with a differential value, or an integral value, or the both values of the output, and the second coefficient table 26c is such rewritten which an error therebetween becomes smaller, whereby a signal to be inputted to the amplifier 12 is corrected.

[0143] As above, an effect of higher-order distortion at a discontinuous point is eliminated when the amplifier 12 is linearized, it is thereby possible to prevent spurious from spreading in a wideband. Accordingly, distortion compensation without an external RF circuit having a linear amplification characteristic becomes possible, which contributes to a reduction of the power consumption, or a reduction of the circuit size.

[0144] A frequency characteristic in the vicinity of higher harmonic in a bias circuit (not shown) of the amplifier 12, a bias circuit (not shown) in the power circuit, or the matching circuit is compensated, thus the accuracy can be improved. It is also possible to cope with a change in characteristics of the amplifier 12 due to a change in temperature or a change with time.

(C) Others

[0145] In the above embodiments and modifications, the present invention is applicable to a radio receiver other than a radio transmitter.

[0146] The present invention is not limited to the above embodiments and modifications, but may be modified in various ways without departing from the scope of the invention.

[0147] For example, as a frequency of a signal to be transmitted, various band may be used, which becomes possible by beforehand calculating coefficient information according to a band to be used.

[0148] In the way of determining an error for giving the first select signal or the second select signal to the coefficient table 15 (or 15a, or 15b), or the second coefficient table 26c, various values may be set according to design environments. The coefficient tables 15a and 15b may be separately configured.

[0149] As the coefficient information, phase information may be variously combined and used.

Industrial Applicability

[0150] As above, the present invention solves a problem which a spurious signal generates in a wide range of frequency, prevents the circuit size from increasing, and contributes to compactness and a reduction of power consumption of an equipment in the mobile communication field such as a portable telephone or the like. Since distortion compensation becomes possible even if a frequency of reference to the memory is slow, and rewriting of the coefficient table becomes possible, it is possible to cope with a change in characteristics of an amplifier due to a change in temperature or a change with time.

Claims

1. A predistortion-type distortion compensation amplifying apparatus comprising:

an amplifier (12) for amplifying a signal to be transmitted; and
a signal processing unit (8, 8a, 8b, 8c, 30, 31) provided on the front side of said amplifier (12) to perform a coefficient changing process to change coefficient information of a power component of an amplitude quantity contained in said signal to be transmitted, and being operable to output a processed signal.

2. The predistortion-type distortion compensation amplifying apparatus according to claim 1, wherein said signal processing unit (30, 31) performs a process with a function representing a reverse characteristic of an input-output characteristic of said amplifier (12) on said signal to be transmitted, performs said coefficient changing process, and makes an odd-order component appear in an output signal of said amplifier (12); and
said amplifier (12) operates in a non-saturation region.

3. The predistortion-type distortion compensation amplifying apparatus according to claim 2, wherein said signal processing unit (30) comprises:

a non-linear processing unit (41) for performing a process with a function representing a reverse characteristic of the input-output characteristic of said amplifier (12) which has been beforehand obtained on said signal to be transmitted, and outputting said signal;
a frequency component attenuating unit (33) for converting a signal outputted from said non-linear processing unit (41) into a frequency domain, attenuating a desired frequency component, reversely converting an attenuated signal into a time domain, and outputting a converted signal; and
a coefficient multiplying unit (34) for comparing said converted signal outputted from said frequency component attenuating unit (33) with said signal to be transmitted, multiplying a power component of odd order of said amplitude quantity by said desired coefficient information, and outputting a result as said processed signal.

4. The predistortion-type distortion compensation amplifying apparatus according to claim 2, wherein said signal processing unit (31) comprises a differential/integral coefficient information adding unit (26) being operable to calculate first differential/integral information based on an amplitude quantity of said signal to be transmitted, calculate second differential/integral information from an amplitude quantity of a signal outputted from said amplifier (12), and input second coefficient information obtained based on said first differential/integral information and said second differential/integral information to said coefficient multiplying unit (34).

5. The predistortion-type distortion compensation amplifying apparatus according to claim 1, wherein said signal processing unit (8, 8a, 8b, 8c) decreases contribution of a power component of odd order of an amplitude quantity contained in said signal to be transmitted when performing said coefficient changing process.

6. The predistortion-type distortion compensation amplifying apparatus according to claim 5, wherein said signal processing unit (8, 8a, 8b, 8c) is configured as a signal amplitude change processing unit (10) being operable to such output said processed signal which contribution of a power component of odd order of an amplitude quantity contained in said signal to be transmitted is decreased;

said signal amplitude change processing unit (10) comprises:

an odd-order component calculating unit (14) for extracting a power component of odd order of the amplitude quantity contained in said signal to be transmitted, and outputting it;
a coefficient information outputting unit (15) being operable to select one of plural pieces of said coefficient information according to a signal from the outside, and output the selected one;
a multiplying unit (16) for multiplying a signal outputted from said odd-order component calculating unit (14) by said coefficient information from said coefficient information outputting unit (15), and outputting an attenuation signal; and
an adding unit (17) for subtracting said attenuation signal from said signal to be transmitted, and outputting a result.

7. The predistortion-type distortion compensation amplifying apparatus according to claim 6, wherein said signal processing unit (8a, 8b, 8c) comprises a control unit (20) being operable to variably adjust a magnitude of said processed signal and input said processed signal to said amplifier (12), and input a first address signal for selecting

coefficient information of said power component of odd order to said signal amplitude change processing unit (10).

8. The predistortion-type distortion compensation amplifying apparatus according to claim 6 or 7, wherein said signal processing unit (8b, 8c) comprises an amplitude information comparing unit (21) being operable to input a first select signal obtained based on said signal to be transmitted and a signal outputted from said amplifier (12) to said signal amplitude change processing unit (10).

9. The predistortion-type distortion compensation amplifying apparatus according to any one of claims 6 through 8, wherein said signal processing unit (8c) comprises a differential/integral coefficient information adding unit (26) being operable to calculate first differential/integral information based on the amplitude quantity of said signal to be transmitted, calculate second differential/integral information from an amplitude quantity of a signal outputted from said amplifier (12), and input second coefficient information obtained based on said first differential/integral information and said second differential/integral information to said signal amplitude change processing unit (10).

10. The predistortion-type distortion compensation amplifying apparatus according to any one of claims 7 through 9, wherein said control unit (20) comprises:

a variable attenuating unit (19) being operable to variably adjust magnitude of said processed signal according to a control signal based on the outside, and input said processed signal to said amplifier (12); and
an output power control unit (18) being operable to control said variable attenuating unit (19), and input a first address signal for selecting said coefficient information to said signal amplitude change processing unit (10).

11. The predistortion-type distortion compensation amplifying apparatus according to claim 6, wherein said odd-order component calculating unit (14) comprises a plurality of sub odd-order component calculating units (14a, 14b) for extracting power components for respective odd-order components of the amplitude quantity contained in said signal to be transmitted, and outputs said power components.

12. The predistortion-type distortion compensation amplifying apparatus according to claim 8, wherein said amplitude information comparing unit (21) comprises:

an output amplitude calculating unit (21c) for extracting an amplitude quantity based on a signal outputted from said amplifier (12), and outputting output amplitude information;
an input amplitude calculating unit (21a) for extracting an amplitude quantity from said signal to be transmitted, and outputting input amplitude information; and
a comparing-rewriting unit (21b) being operable to input said first select signal to said signal amplitude change processing unit (10) according to an error between said output amplitude information (21c) and said input amplitude information (21a).

13. The predistortion-type distortion compensation amplifying apparatus according to claim 12, wherein said comparing-rewriting unit (21b) inputs said first select signal to said signal amplitude change processing unit (10) in order to such rewrite said coefficient information which said coefficient information becomes larger when said input amplitude information is smaller than said output amplitude information, whereas inputting said first select signal to said signal amplitude change processing unit (10) in order to such rewrite said coefficient information which said coefficient information becomes smaller when said input amplitude information is larger than said output amplitude information.

14. The predistortion-type distortion compensation amplifying apparatus according to claim 4, wherein said differential/integral coefficient information adding unit (26) comprises:

a first differential/integral information calculating unit (26a) being operable to calculate first differential/integral information based on the amplitude quantity of said signal to be transmitted and output said first differential/integral information, and input a second address signal obtained based on the amplitude quantity of said signal to be transmitted to said second coefficient information outputting unit (26c);
a second differential/integral information calculating unit (26d) for calculating second differential/integral information from an amplitude quantity of a signal outputted from said amplifier (12), and outputting said second differential/integral information;
a second coefficient information outputting unit (26c) being operable to select one of plural pieces of coefficient information according to said second address signal, and output the selected one as second coefficient information.

mation to said coefficient multiplying unit (34); and
 a second comparing-rewriting unit (26b) being operable to rewrite said second coefficient information according to an error between said first differential/integral information and said second differential/integral information.

- 5 15. The predistortion-type distortion compensation amplifying apparatus according to claim 9, wherein said differential/integral coefficient information adding unit (26) comprises:

10 a first differential/integral information calculating unit (26a) being operable to calculate first differential/integral information based on the amplitude quantity of said signal to be transmitted and output said first differential/integral information, and input a second address signal obtained from the amplitude quantity of said signal to be transmitted to said second coefficient information outputting unit (26c);

15 a second differential/integral information calculating unit (26d) for calculating second differential/integral information based on an amplitude quantity of a signal outputted from said amplifier (12), and outputting said second differential/integral information;

a second coefficient information outputting unit (26c) being operable to select one of plural pieces of coefficient information according to said second address signal, and output the selected one as second coefficient information to said signal amplitude change processing unit (10); and

20 a second comparing-rewriting unit (26b) being operable to rewrite said second coefficient information according to an error between said first differential/integral information and said second differential/integral information.

- 25 16. The predistortion-type distortion compensation amplifying apparatus according to claim 14 or 15, wherein said second comparing-rewriting unit (26b) compares said first differential/integral information with said second differential/integral information, and such rewrites said second coefficient information which an error therebetween becomes smaller.

FIG. 1

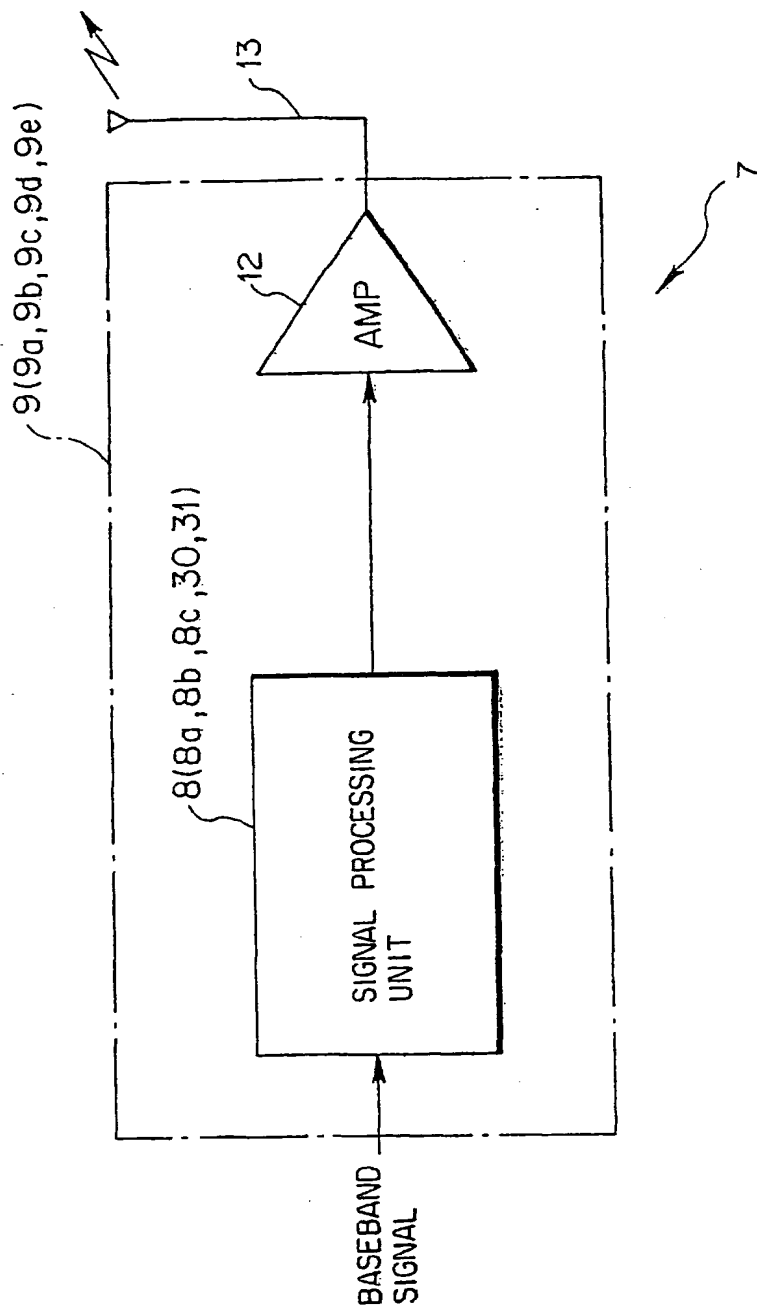


FIG. 2

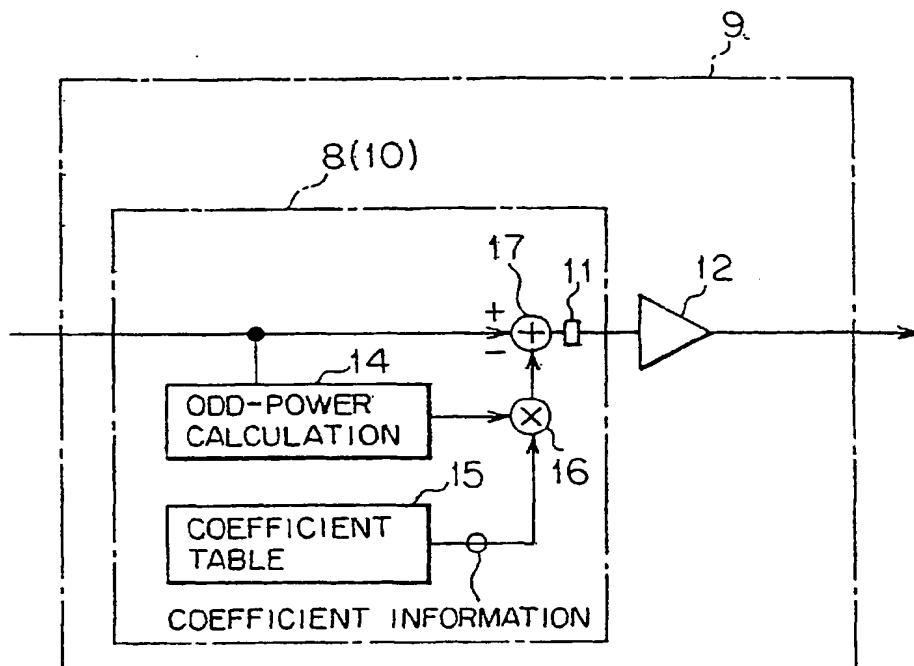


FIG. 3

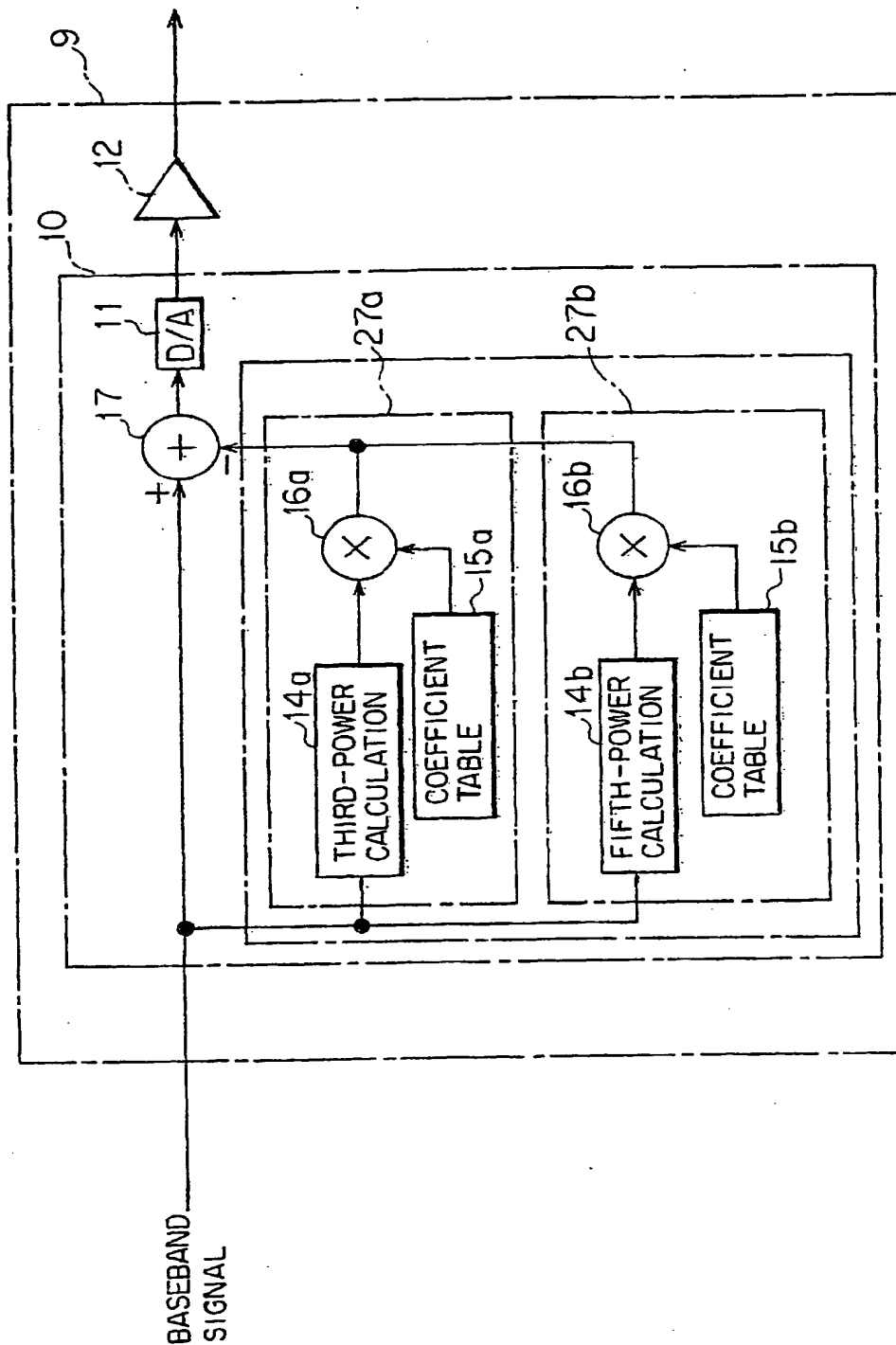


FIG. 4

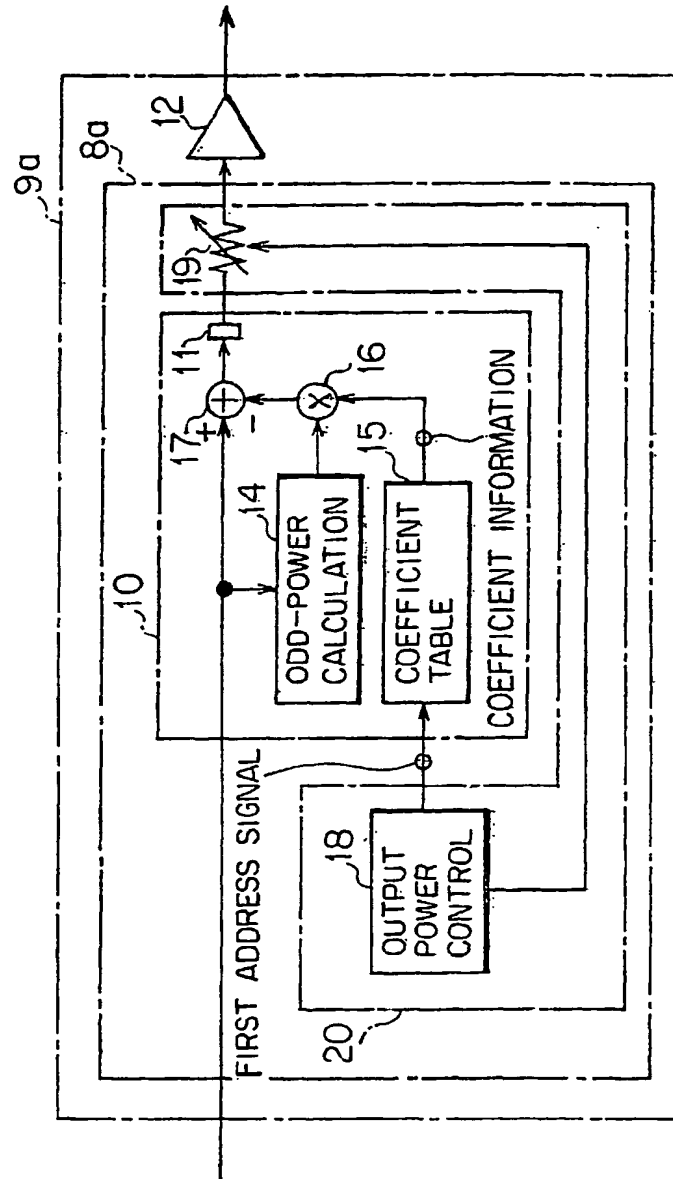


FIG. 5

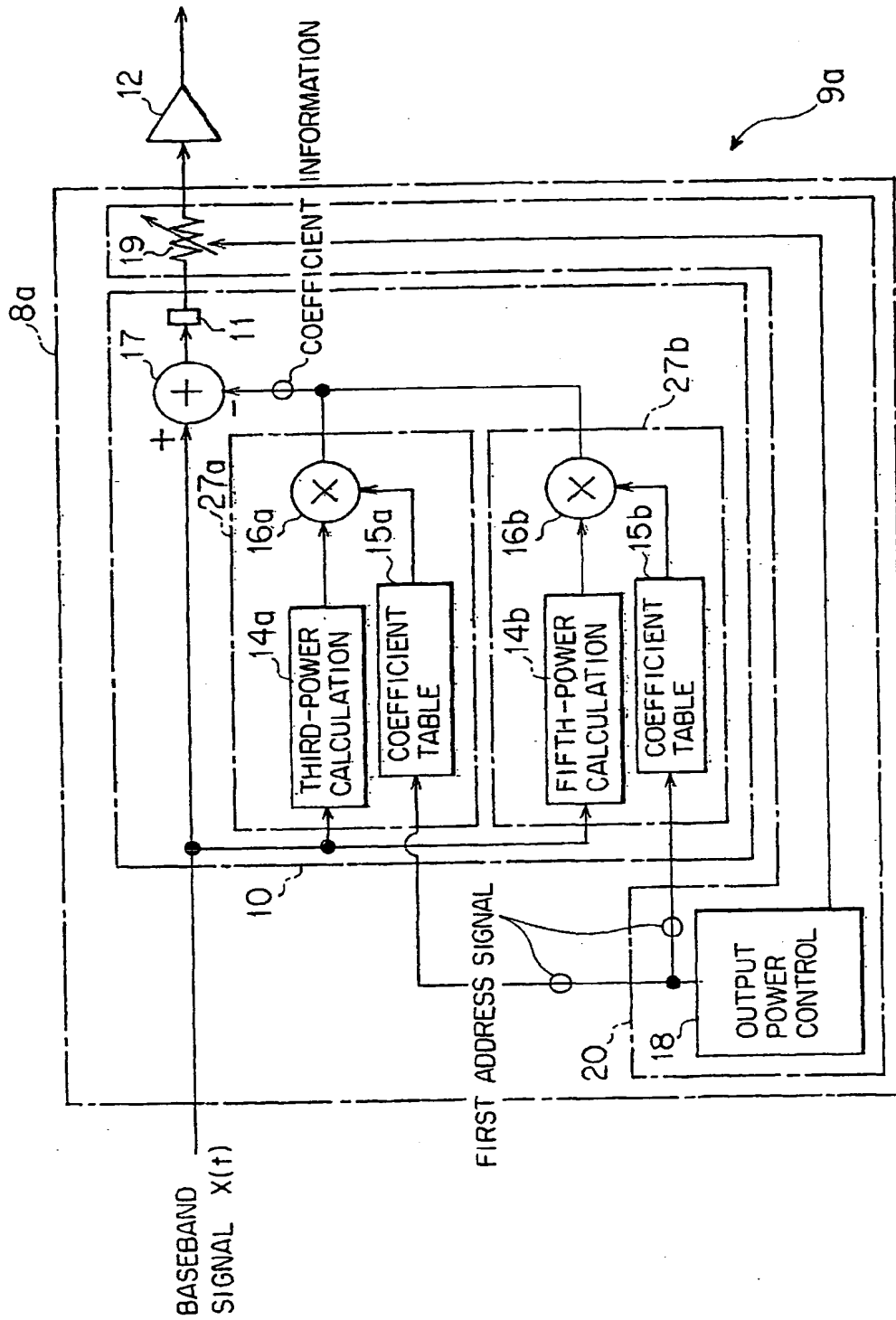


FIG. 7

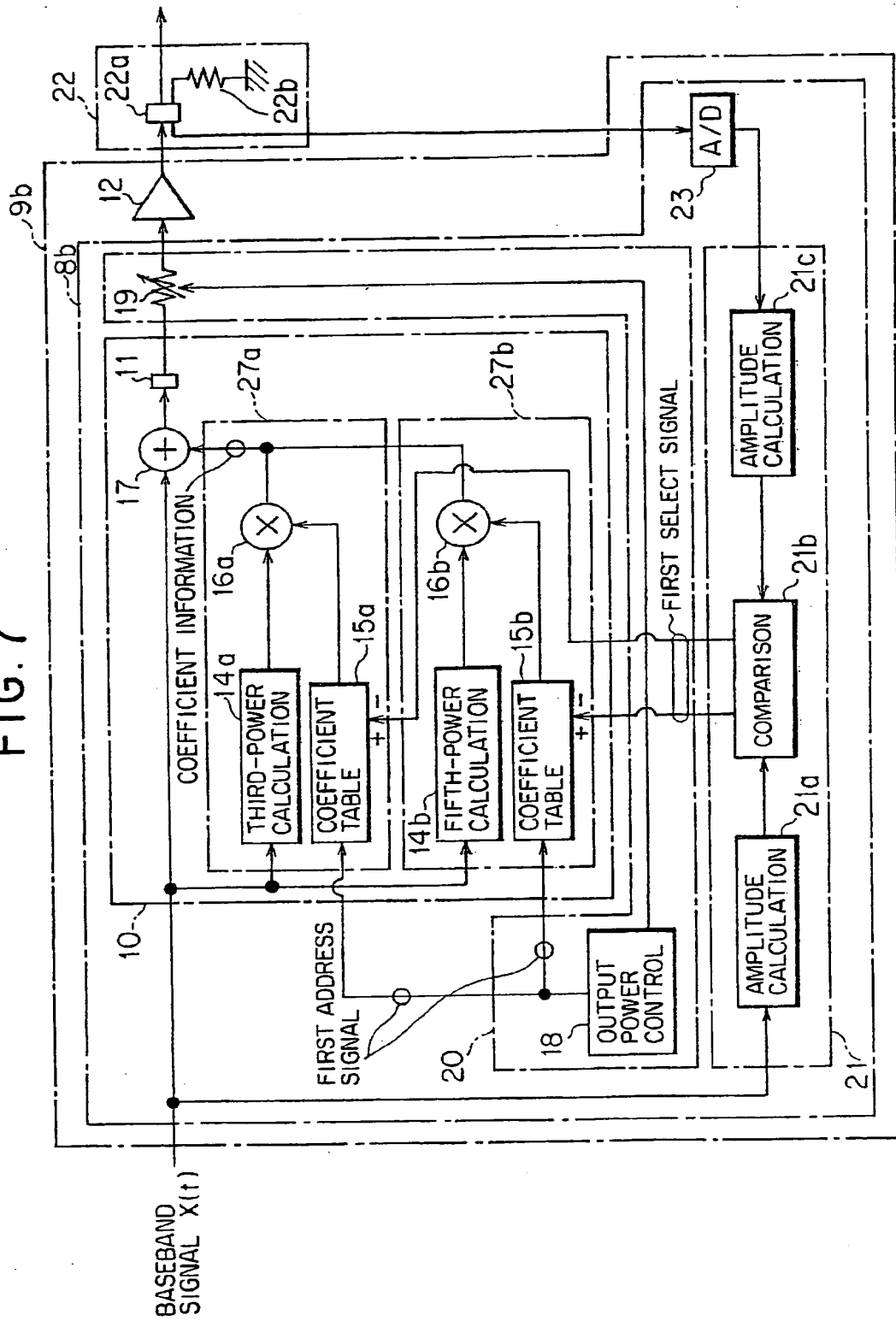


FIG. 8

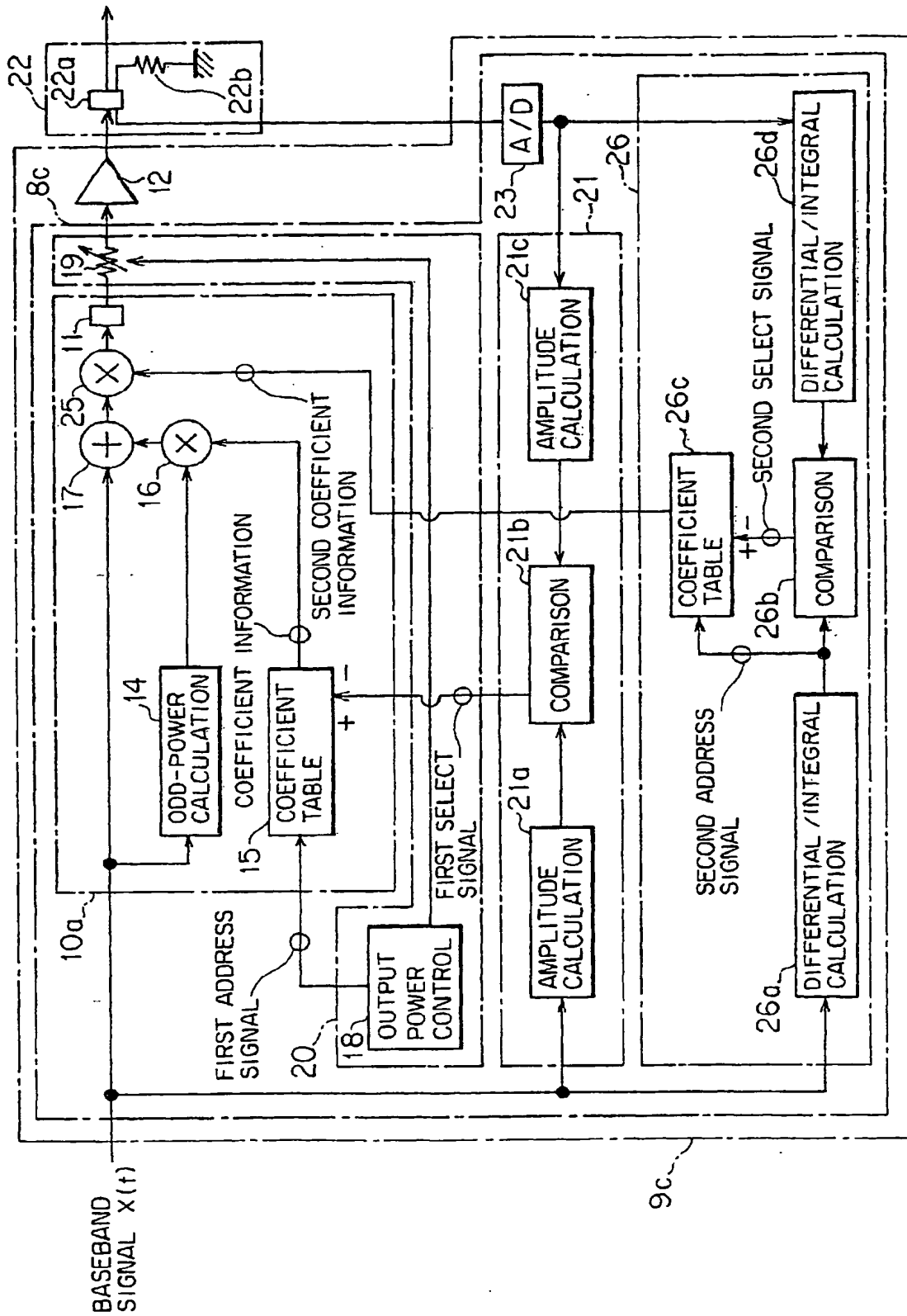


FIG. 9

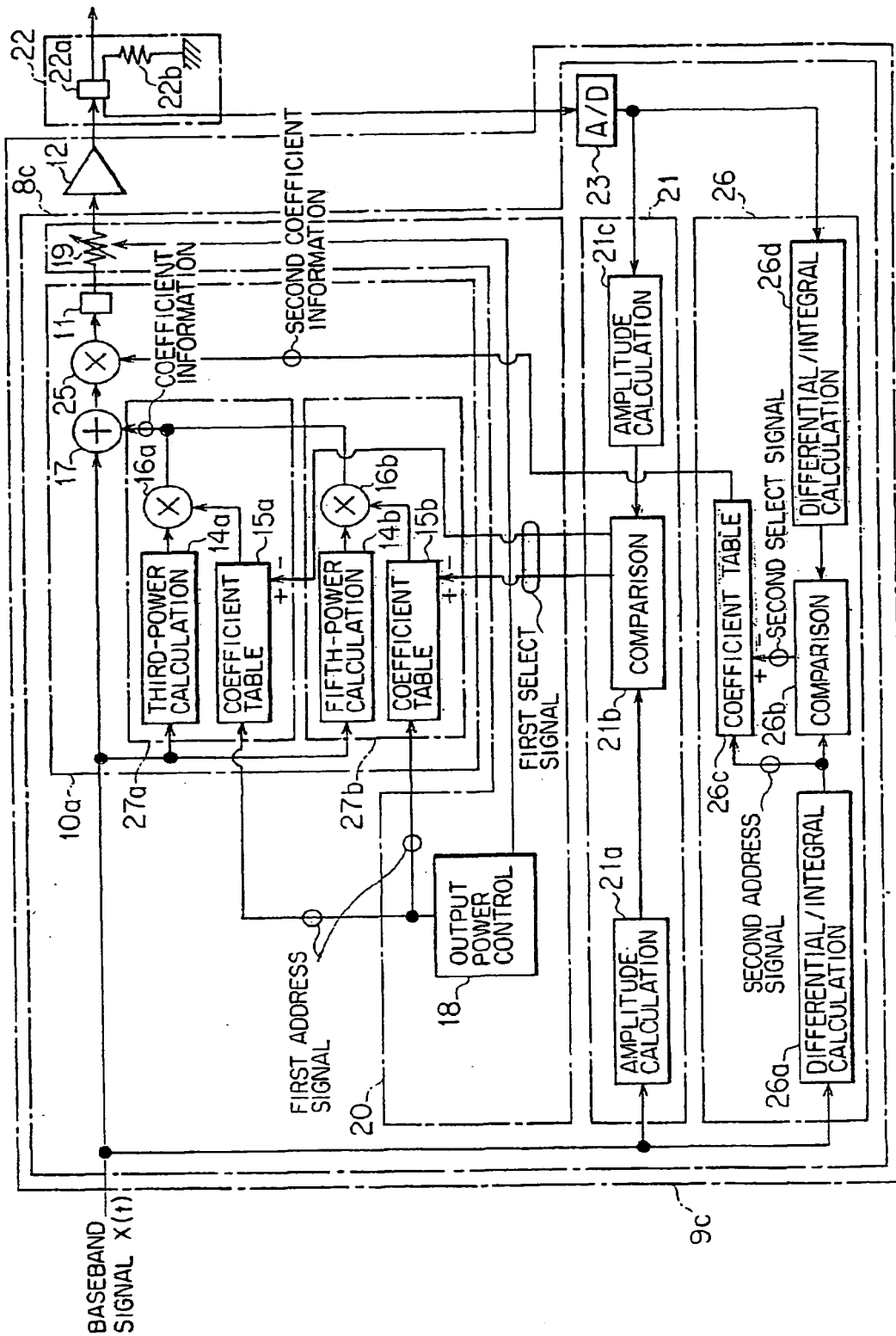


FIG. 10

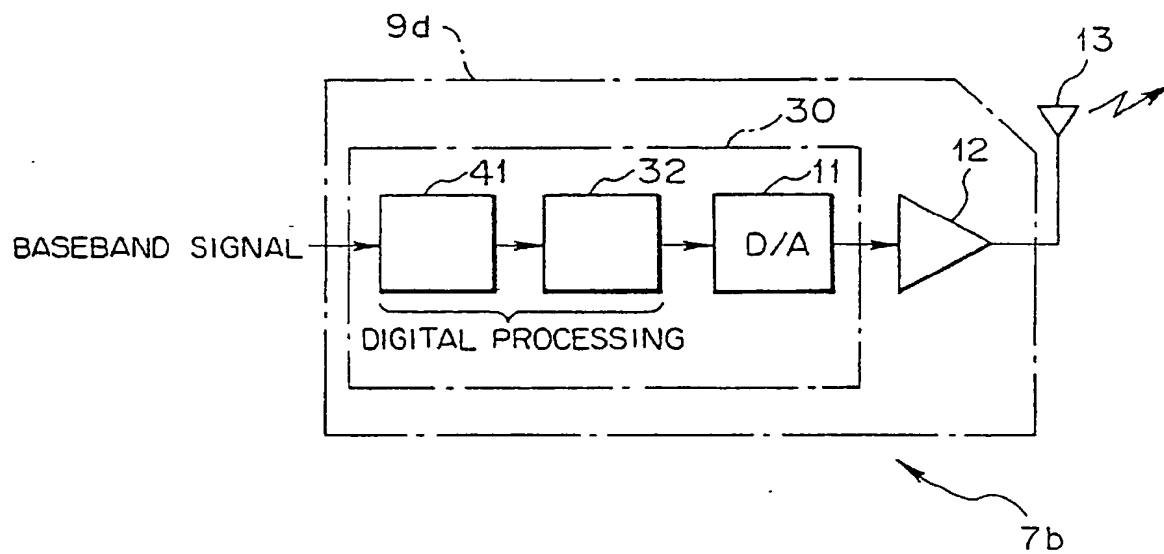


FIG. 11

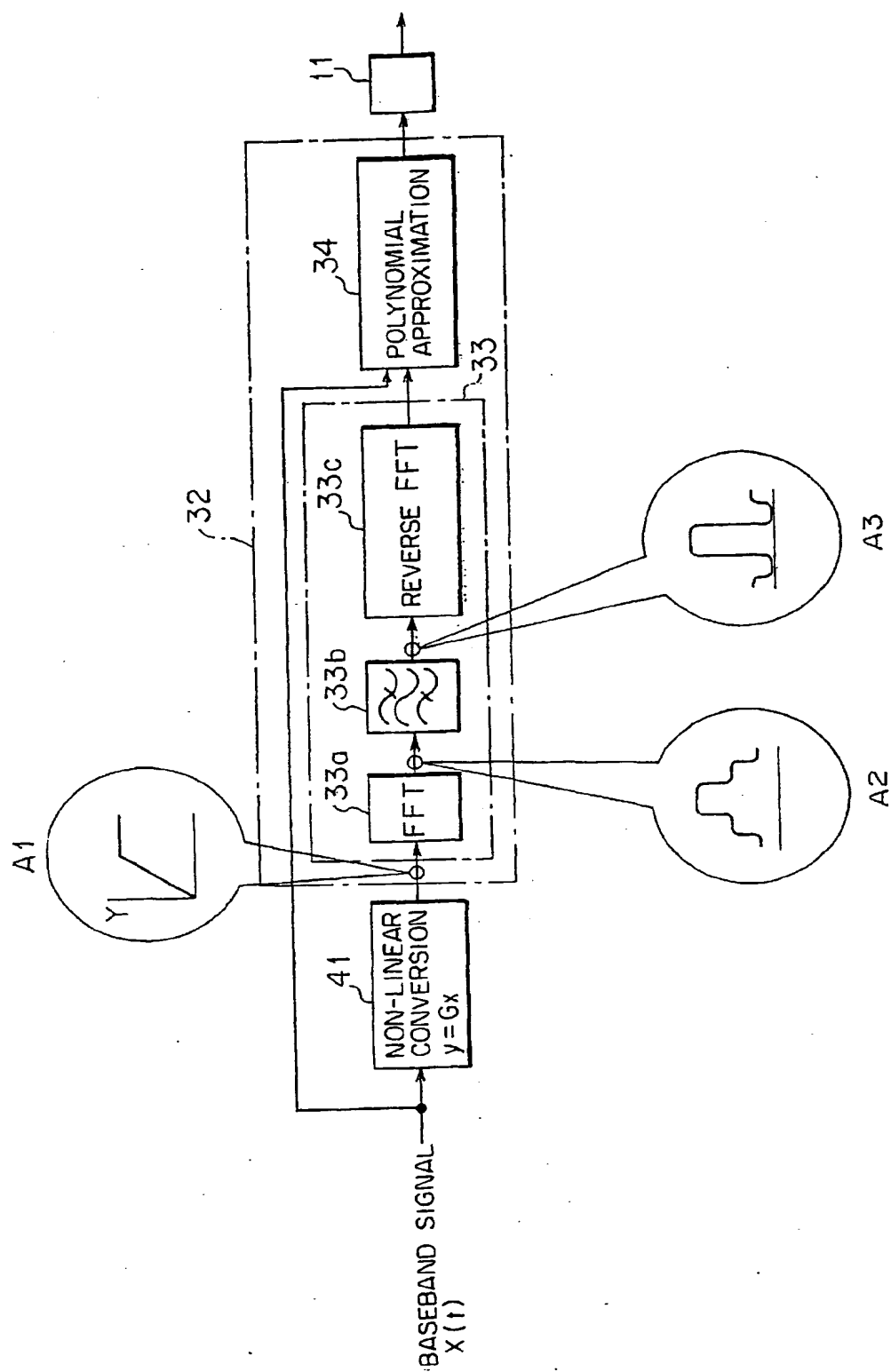


FIG. 12

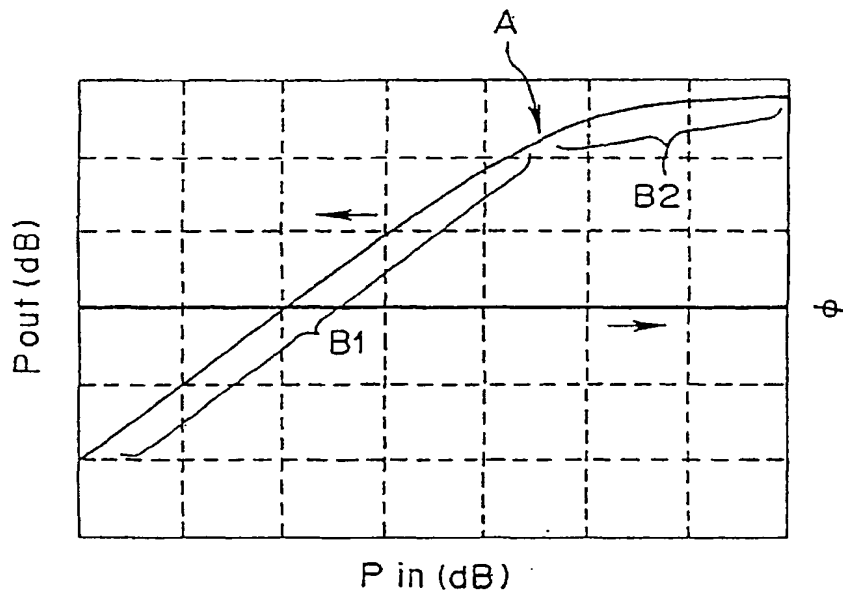


FIG. 13(a)

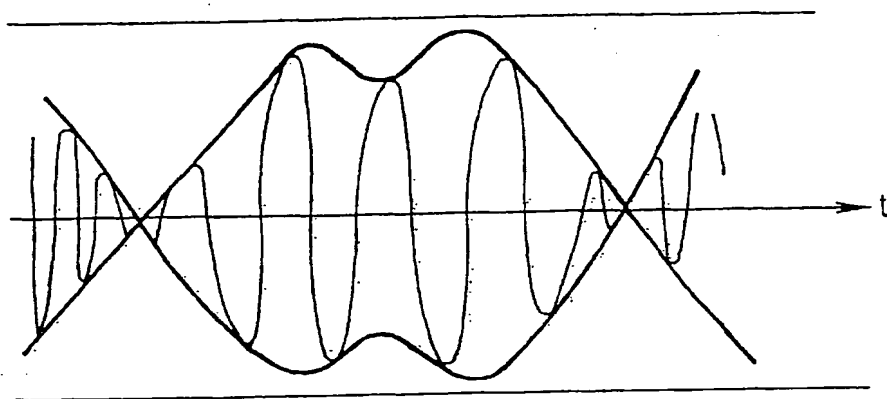


FIG. 13(b)

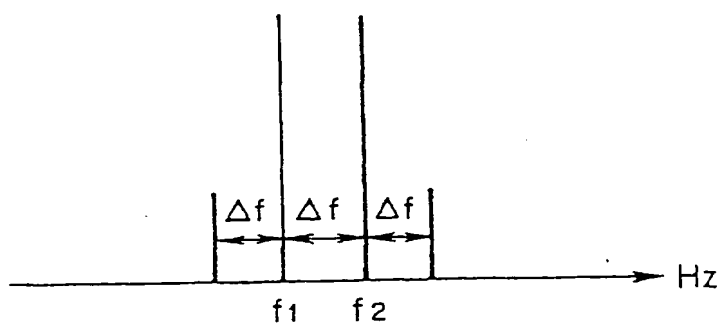


FIG. 14(a)

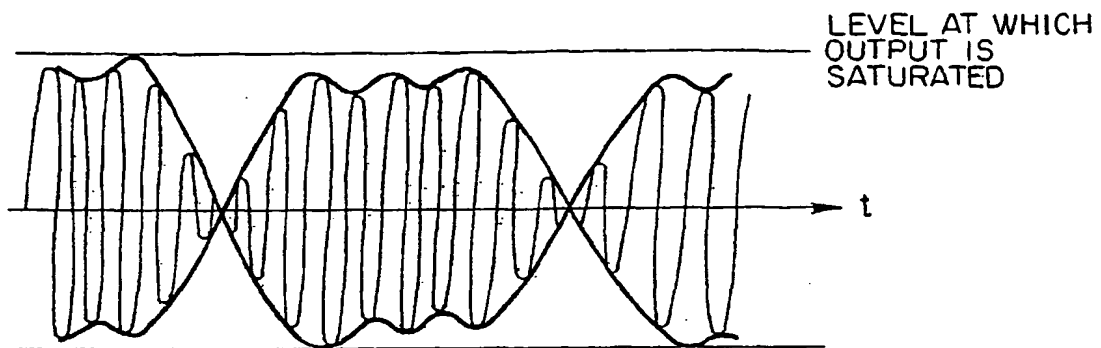


FIG. 14(b)

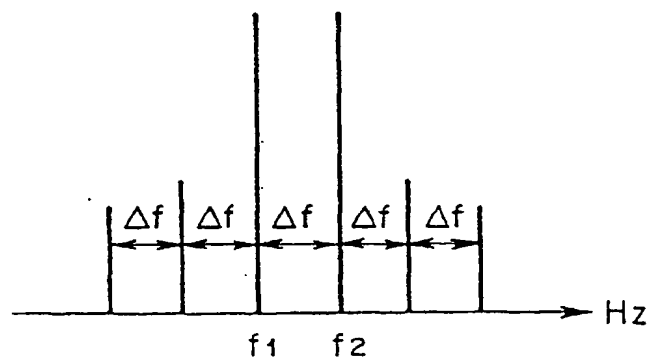


FIG. 15

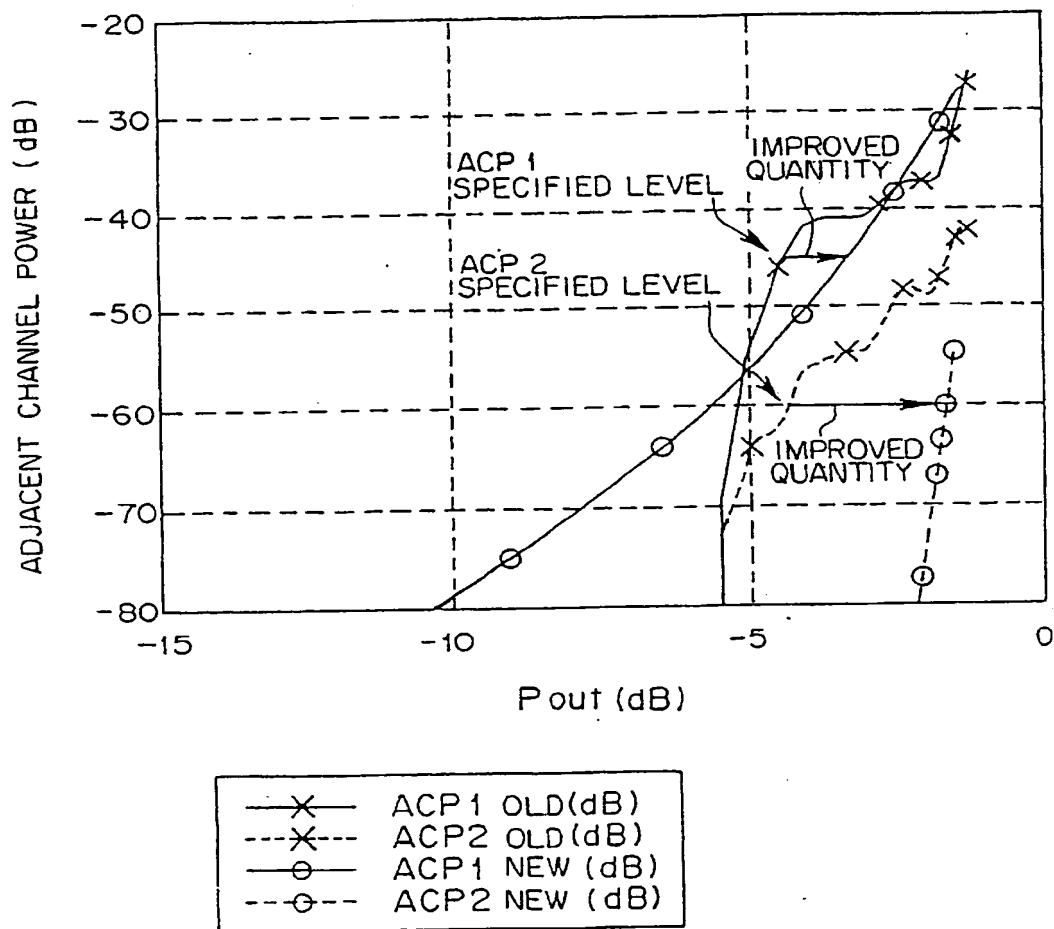


FIG. 16

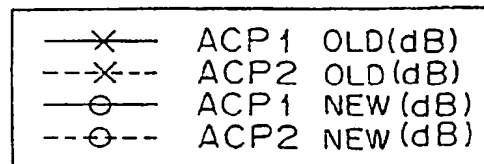
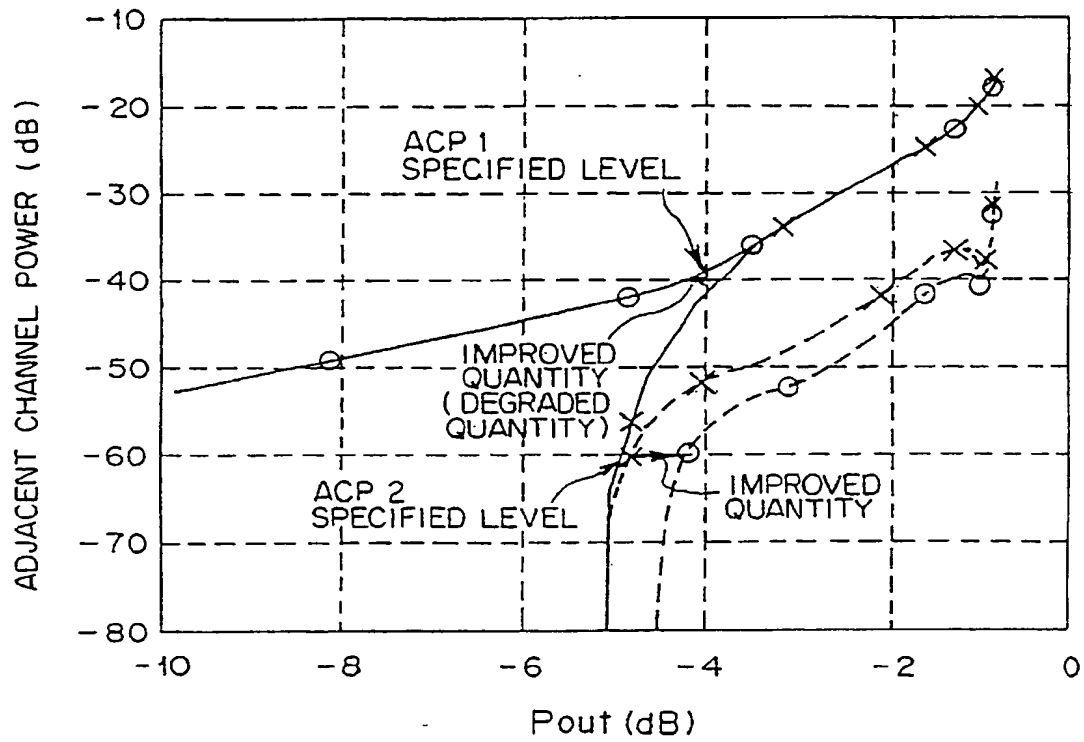


FIG. 17

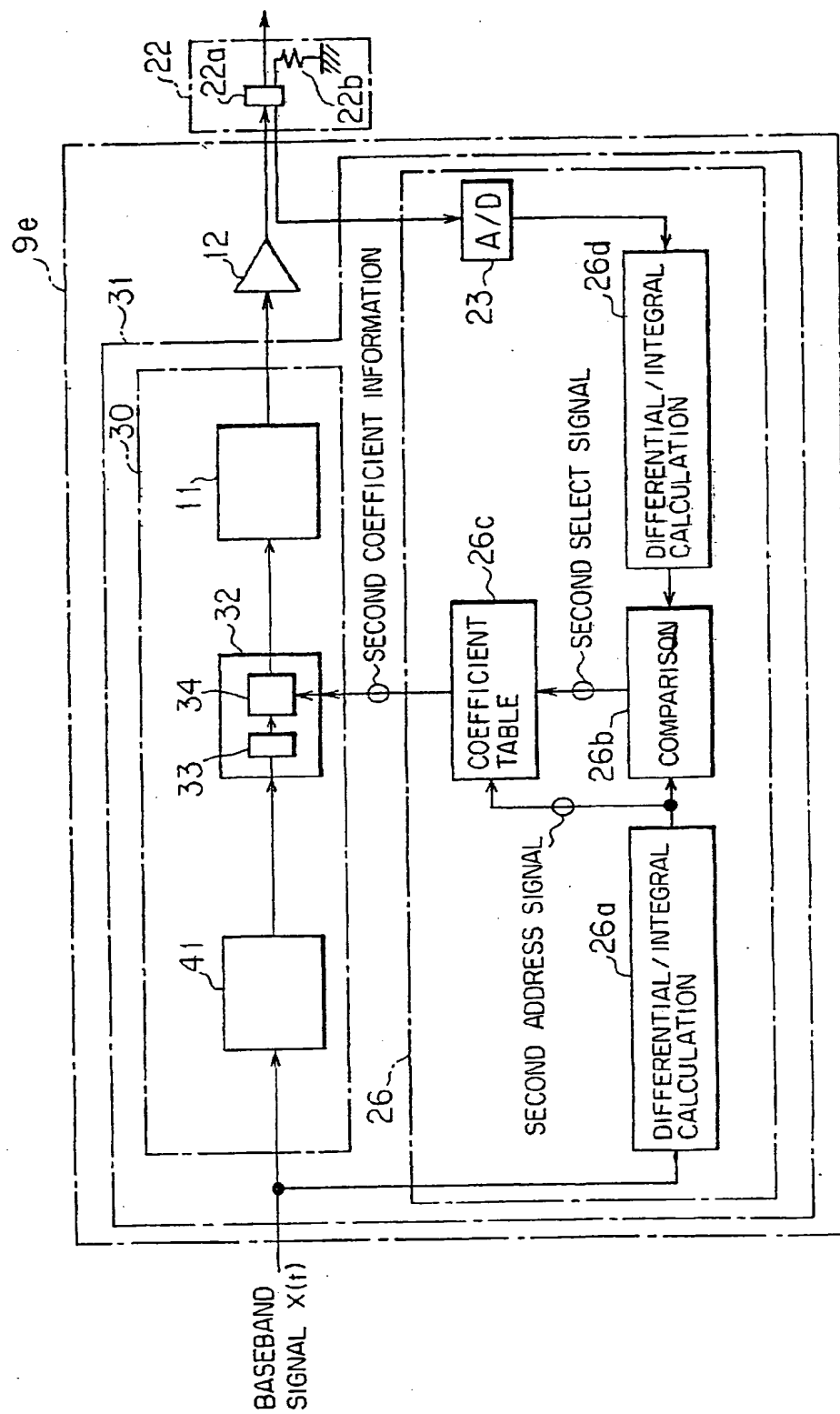


FIG. 18

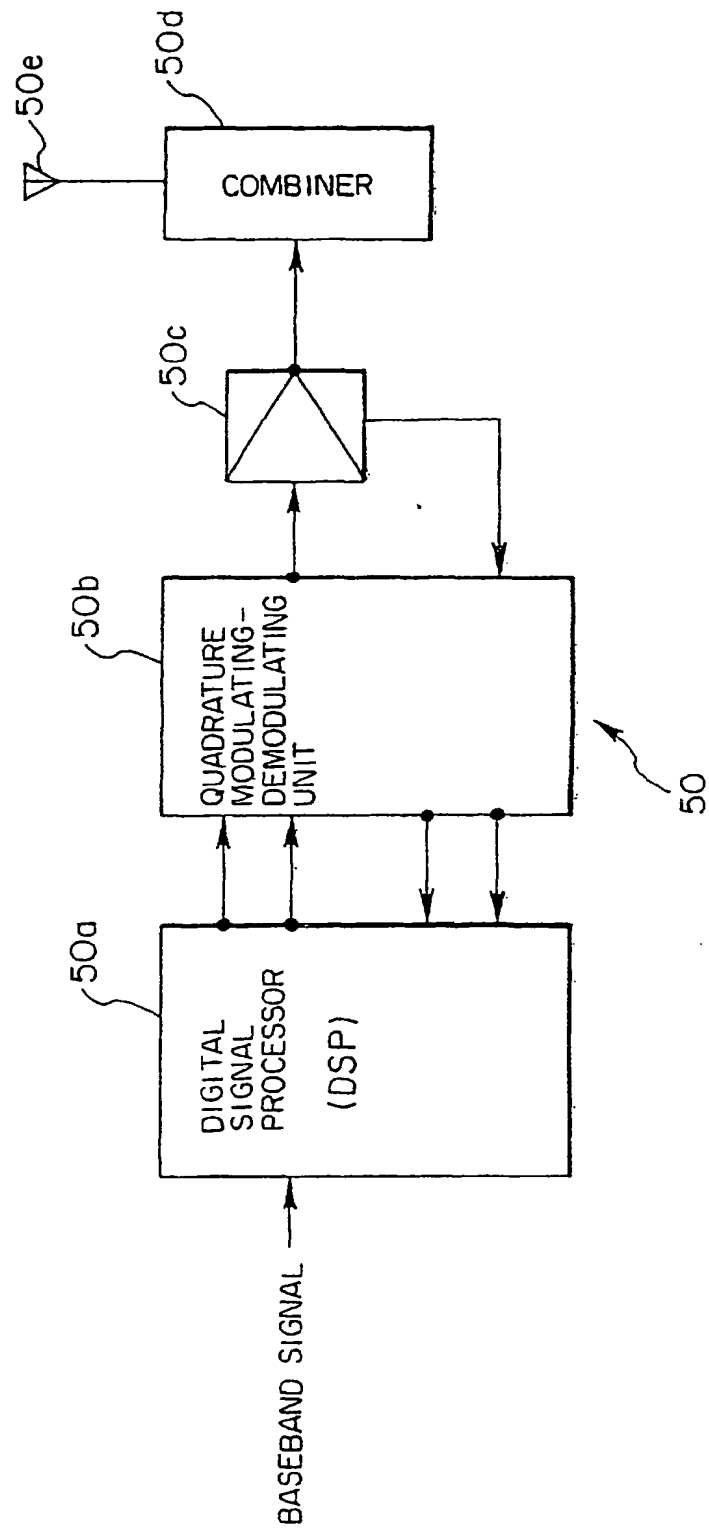


FIG. 19

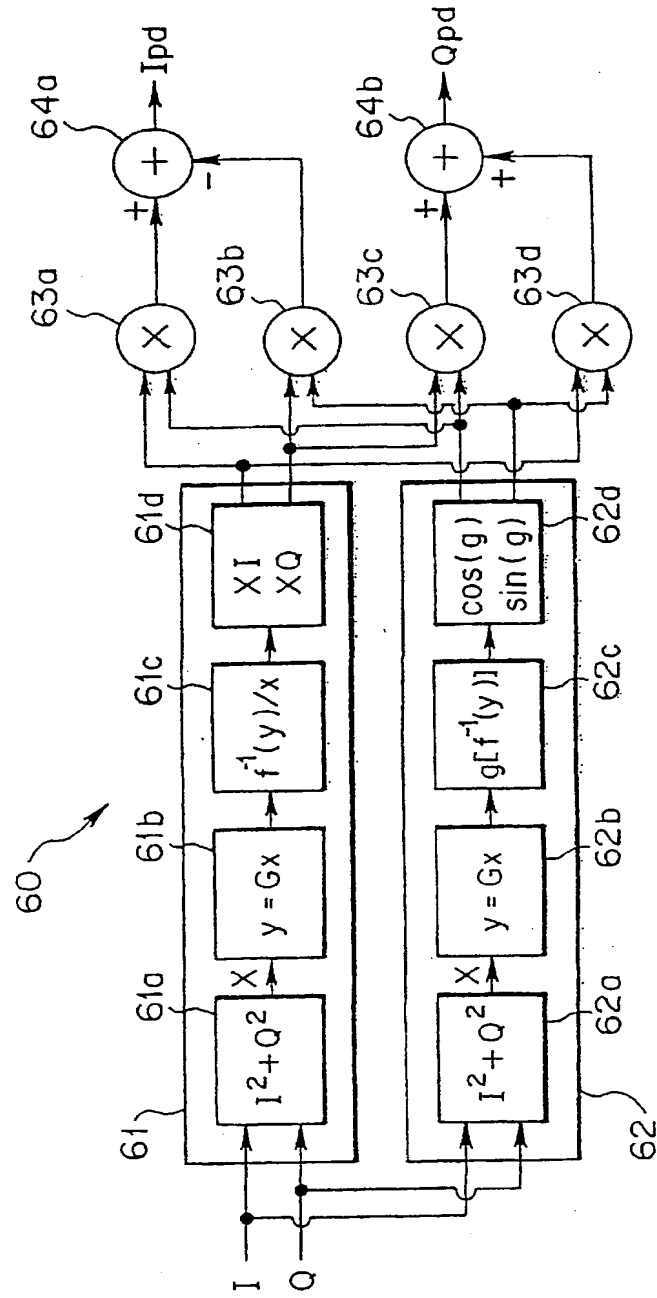


FIG. 20

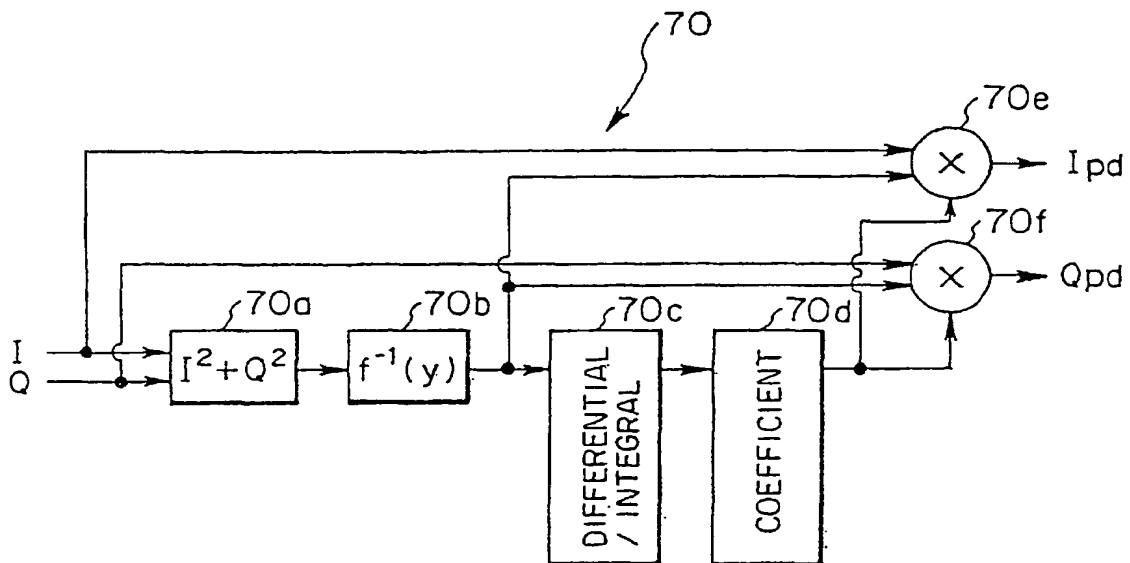


FIG. 21 (a)

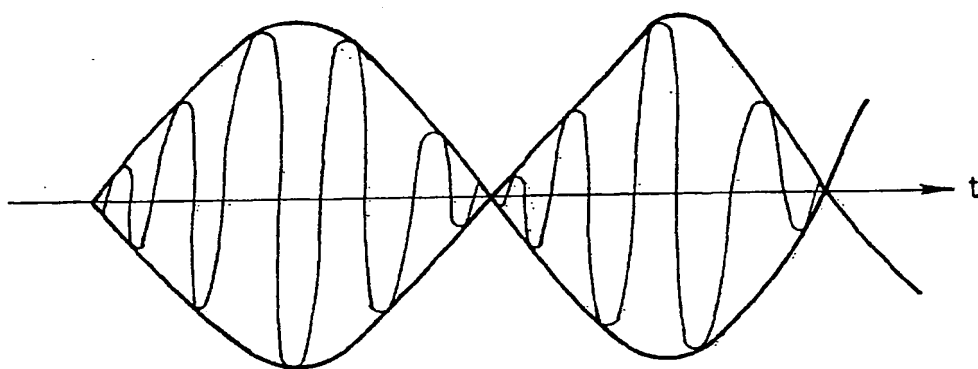


FIG. 21 (b)

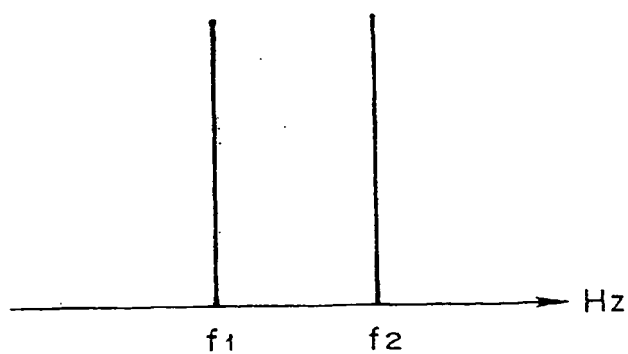


FIG. 22 (a)

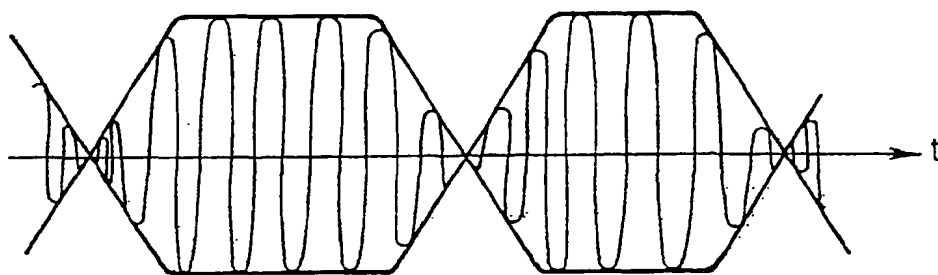


FIG. 22 (b)

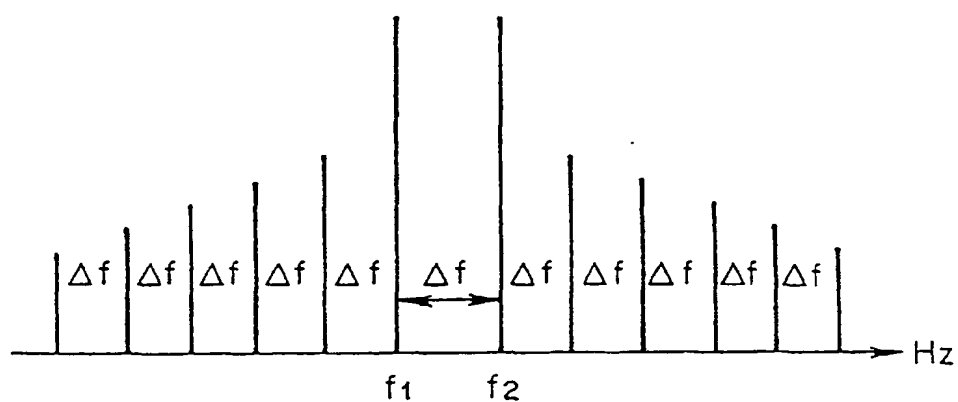
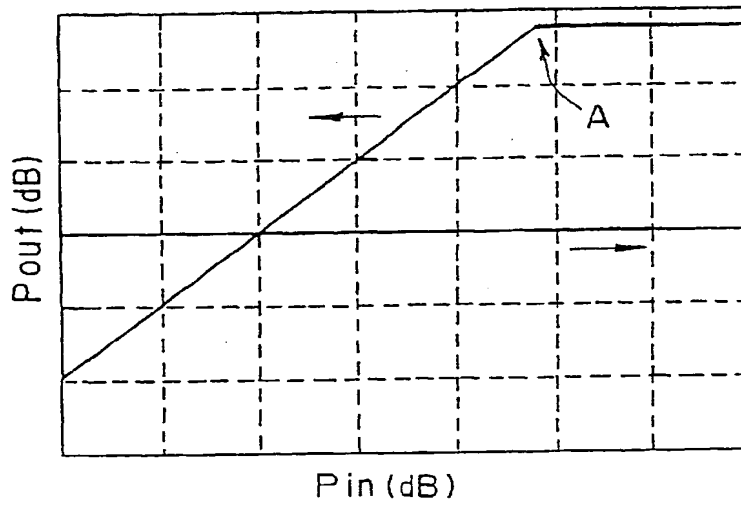


FIG. 23



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP99/02824

A. CLASSIFICATION OF SUBJECT MATTER Int. Cl. ⁶ H03F1/32		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols) Int. Cl. ⁶ H03F1/32		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926-1996 Toroku Jitsuyo Shinan Koho 1994-1999 Kokai Jitsuyo Shinan Koho 1971-1999 Jitsuyo Shinan Toroku Koho 1996-1999		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) WPI		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y	EP, 67091, A (THOMSON-CSF), 15 December, 1982 (15. 12. 82) & FR, 2507026, A & JP, 57-199305, A	1-2 3-16
X Y	EP, 85600, A (THOMSON-CSF), 10 August, 1983 (10. 08. 83) & FR, 2520957, A & JP, 58-134517, A	1-3 4-16
Y A	JP, 4-16006, A (Kokusai Electric Co., Ltd.), 21 January, 1992 (21. 01. 92) (Family: none)	4-16 1-3
Y A	US, 4476438, A (U.S. Philips Corporation), 9 October, 1984 (09. 10. 84) & DE, 3142199, A & GB, 2087181, A & JP, 57-106215, A	7-10, 12-16 1-6, 11
Y A	US, 5164678, A (Asea Brown Boveri Ltd), 17 July, 1992 (17. 07. 92) & CA, 204657, A & CN, 1058122, A & CS, 9102104, A & EP, 465709, A & JP, 4-233810, A	7-10, 12-16 1-6, 11
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
<p>* Special categories of cited documents:</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</p> <p>"&" document member of the same patent family</p>		
Date of the actual completion of the international search 15 July, 1999 (15. 07. 99)		Date of mailing of the international search report 3 August, 1999 (03. 08. 99)
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer
Facsimile No.		Telephone No.

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP99/02824

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP, 433863, A (TELETTRA Telefonica Elettronica e Radio S.p.A.), 26 June, 1991 (26. 06. 91) & IT, 1236905, A & JP, 4-211549, A & US, 5204881, A	1-16
A	EP, 441580, A (GEC-MARCONI Limited), 14 August, 1991 (14. 08. 91) & AT, 143197, A & GB, 2240892, A & JP, 6-209218, A & US, 5124665, A	1-16
A	JP, 9-83417, A (Hitachi Electronics, Ltd.), 28 March, 1997 (28. 03. 97) (Family: none)	1-16
A	US, 5798854, A (Ortel Corporation), 25 August, 1998 (25. 08. 98) & AU, 2641895, A & CN, 1151229, A & EP, 760184, A & JP, 10-500824, A & WO, 95/32561, A	1-16

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